

FIG.1 (PRIOR ART)

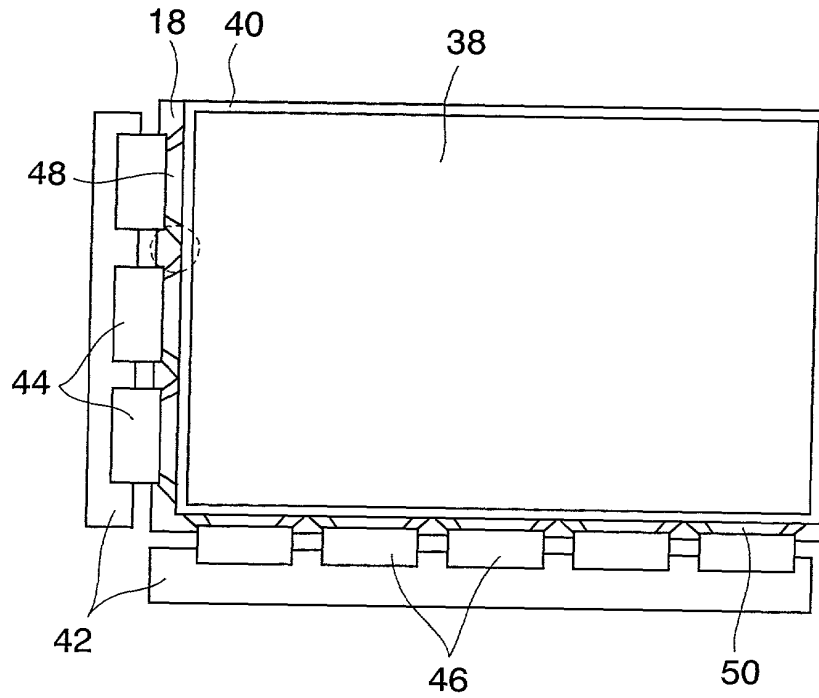


FIG.2 (PRIOR ART)

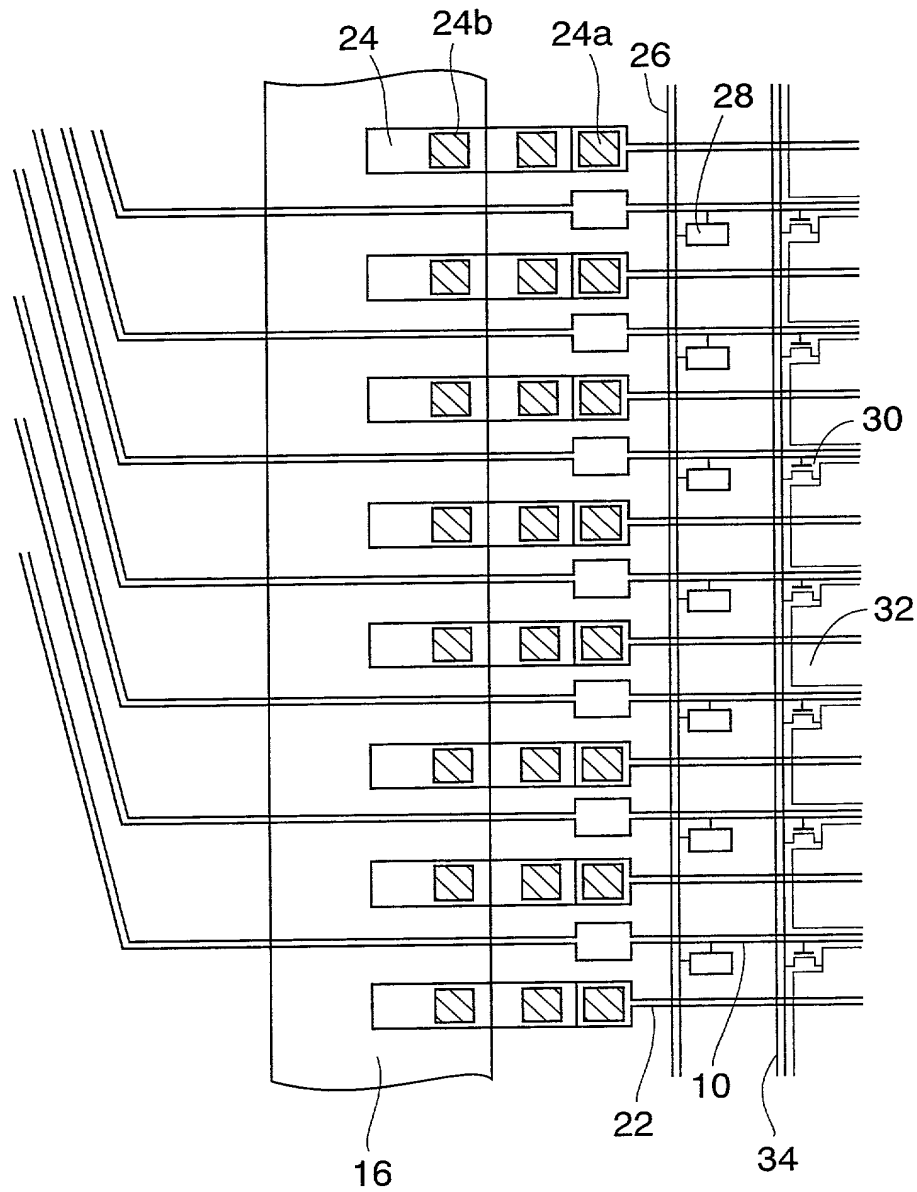


FIG.3 (PRIOR ART)

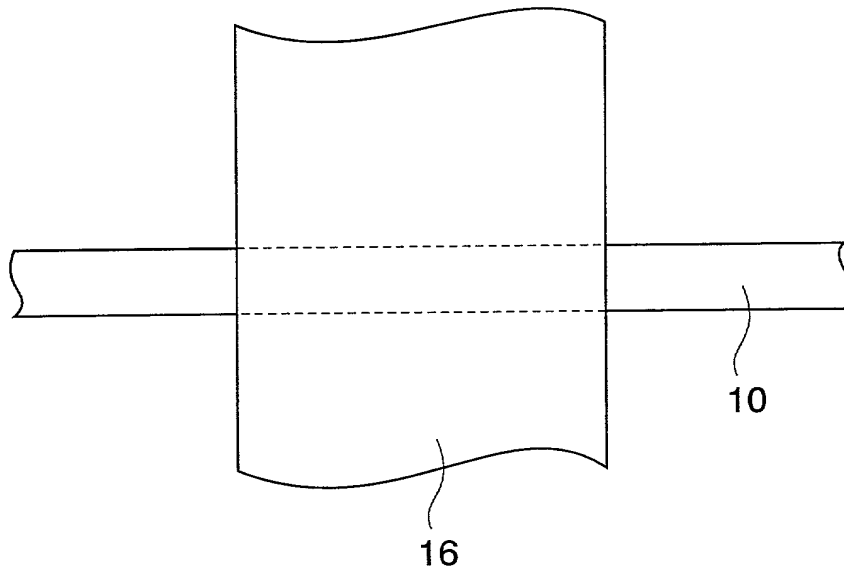


FIG.4 (PRIOR ART)

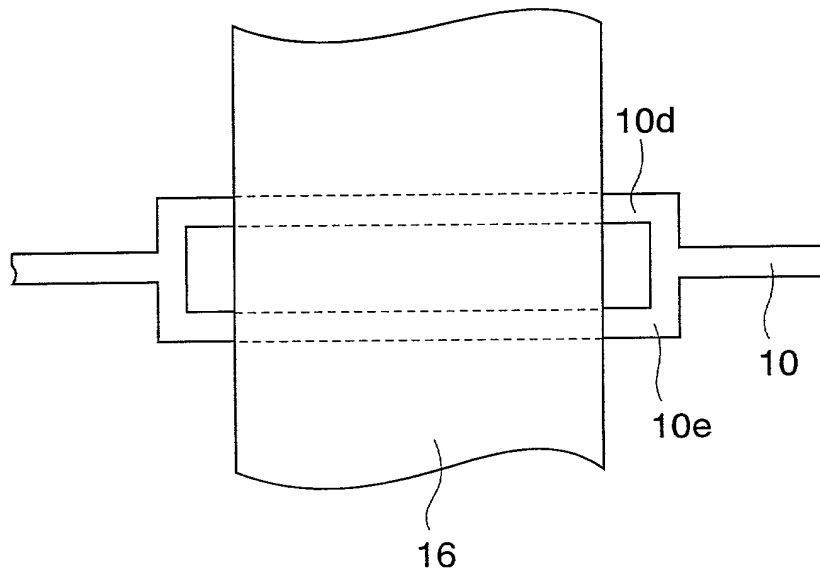
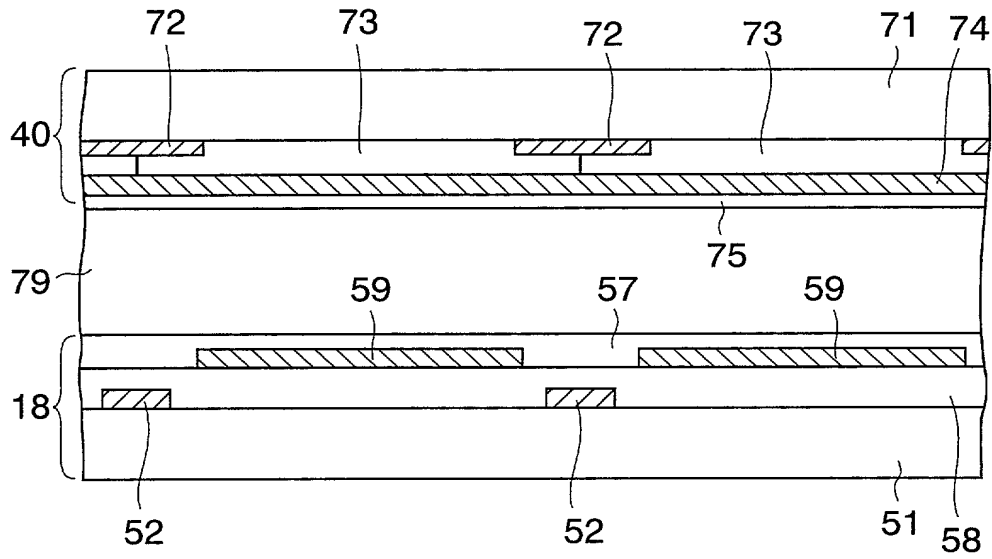


FIG.5 (PRIOR ART)



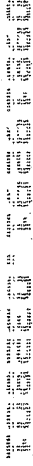
[illegible]

FIG.7A (PRIOR ART)

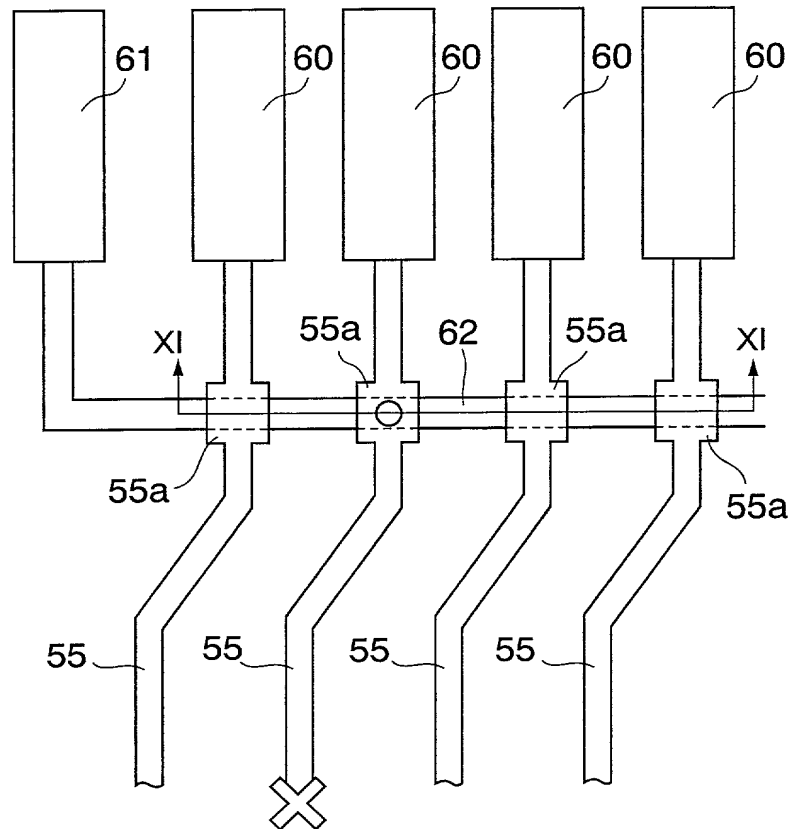


FIG.7B (PRIOR ART)

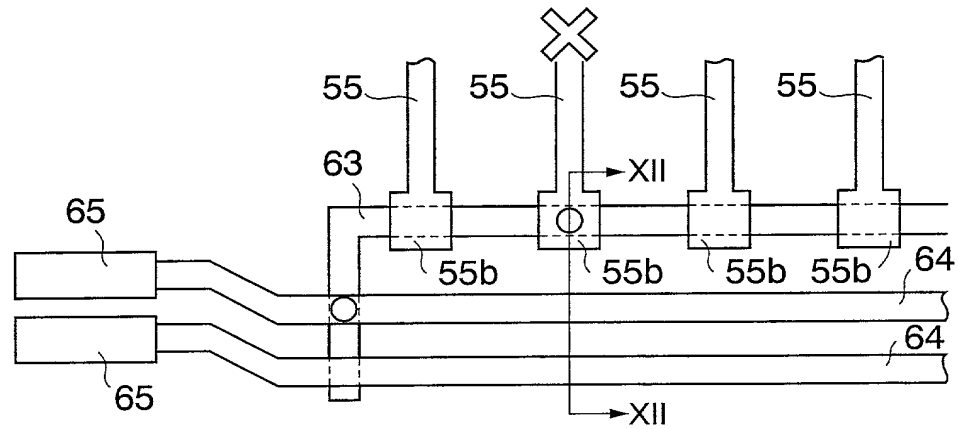


FIG.8A (PRIOR ART)

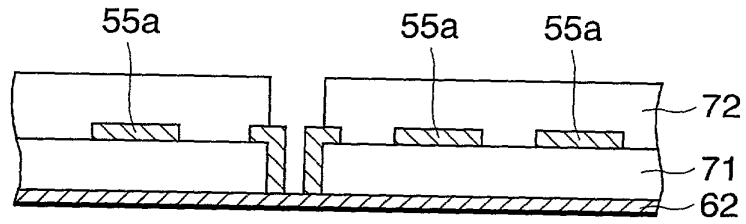


FIG.8B (PRIOR ART)

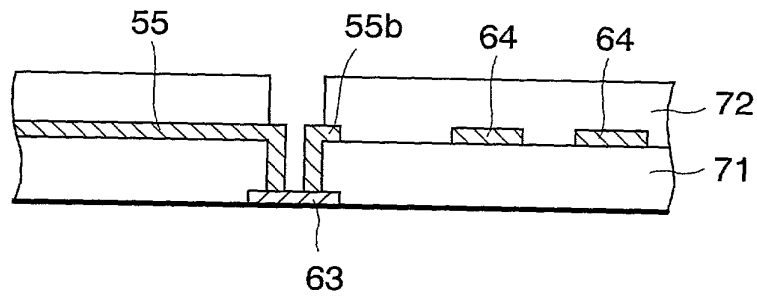






FIG.10A

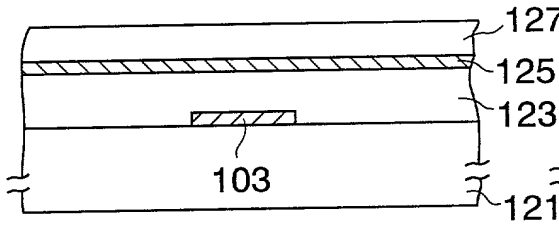


FIG.10B

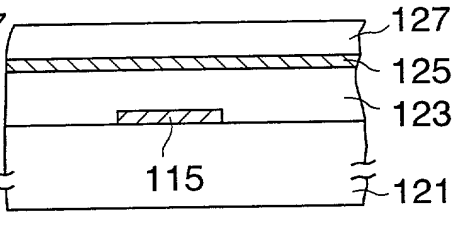


FIG.11A

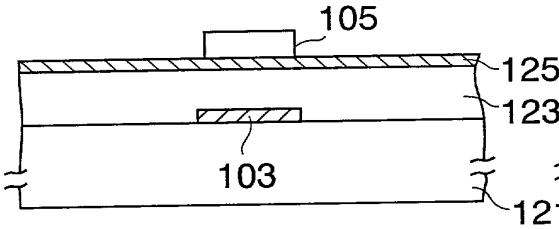


FIG.11B

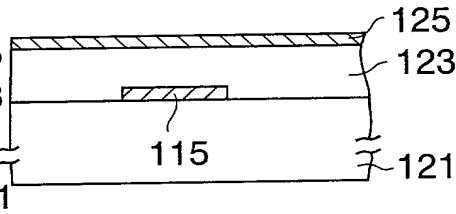


FIG.12A

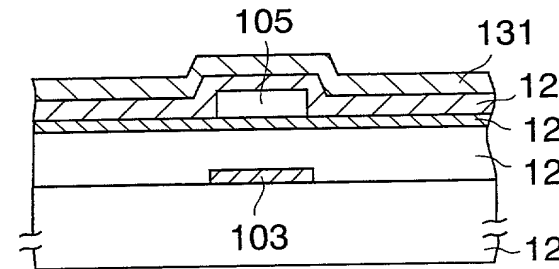


FIG.12B

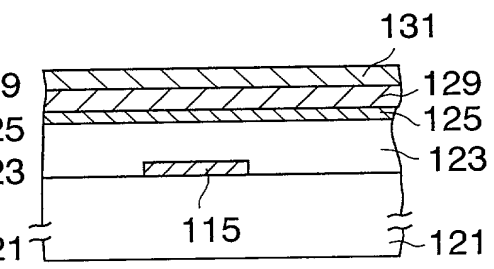


FIG.13A

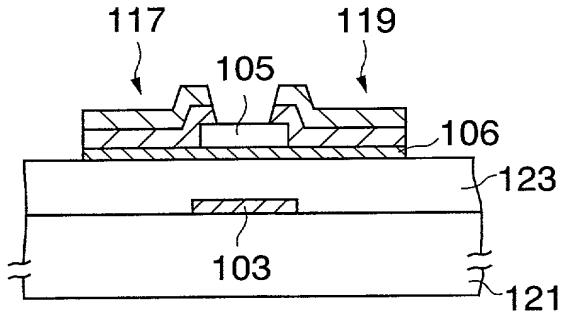


FIG.13B

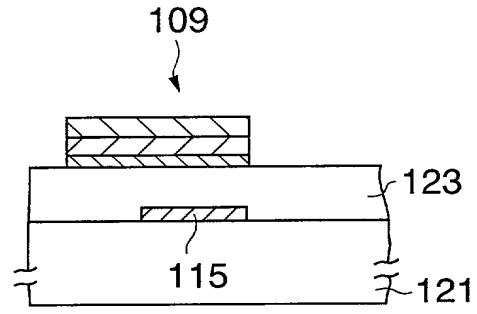


FIG.14A

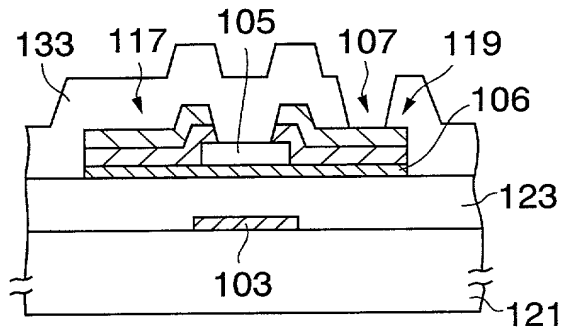


FIG.14B

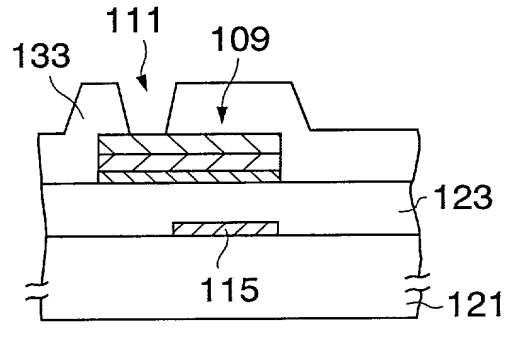


FIG.15A

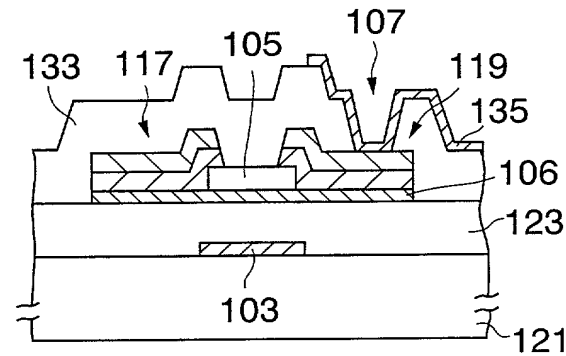


FIG.15B

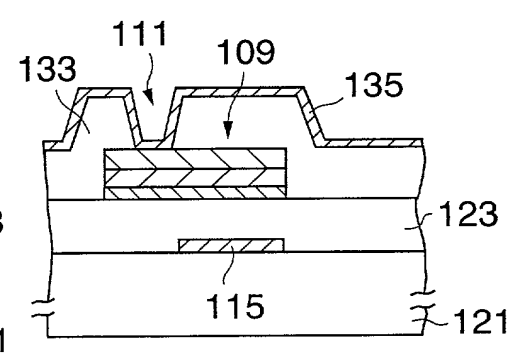




FIG.17A

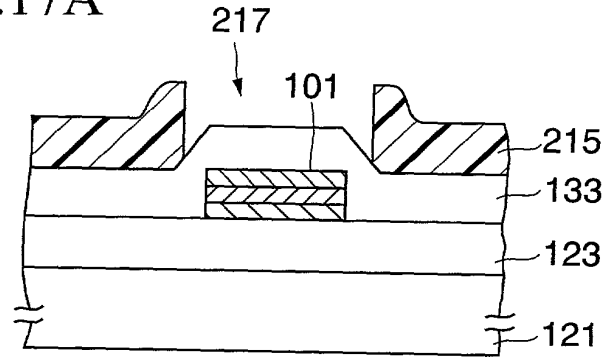


FIG.17B

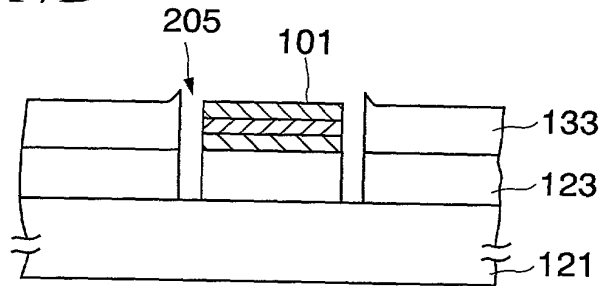


FIG.17C

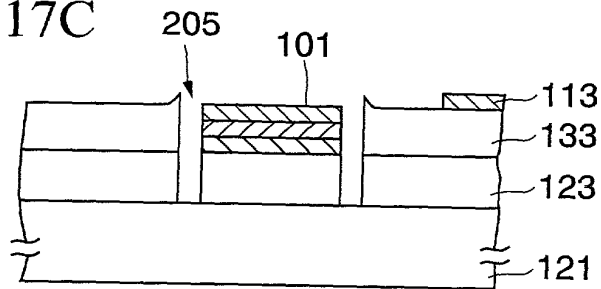


FIG.17D

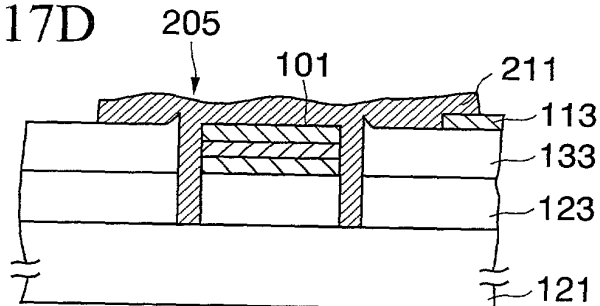




FIG.19A

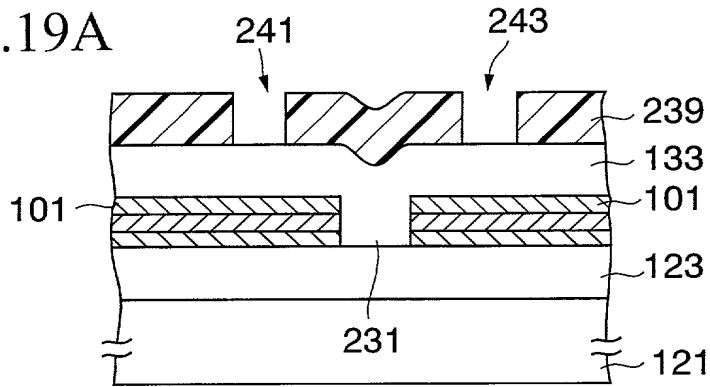


FIG.19B

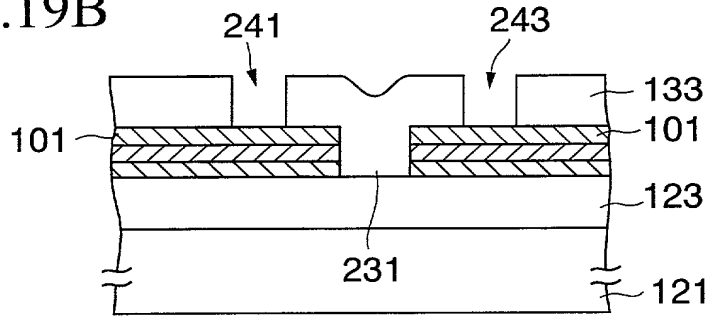


FIG.19C

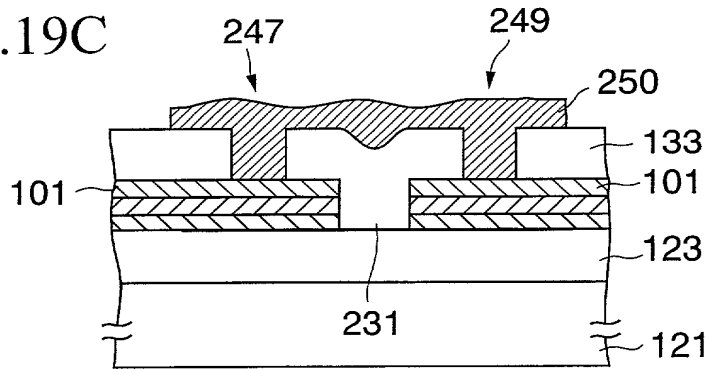


FIG.19D

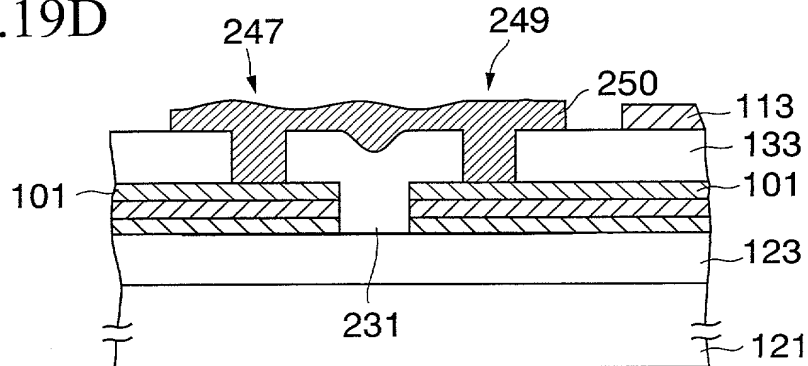




FIG.21

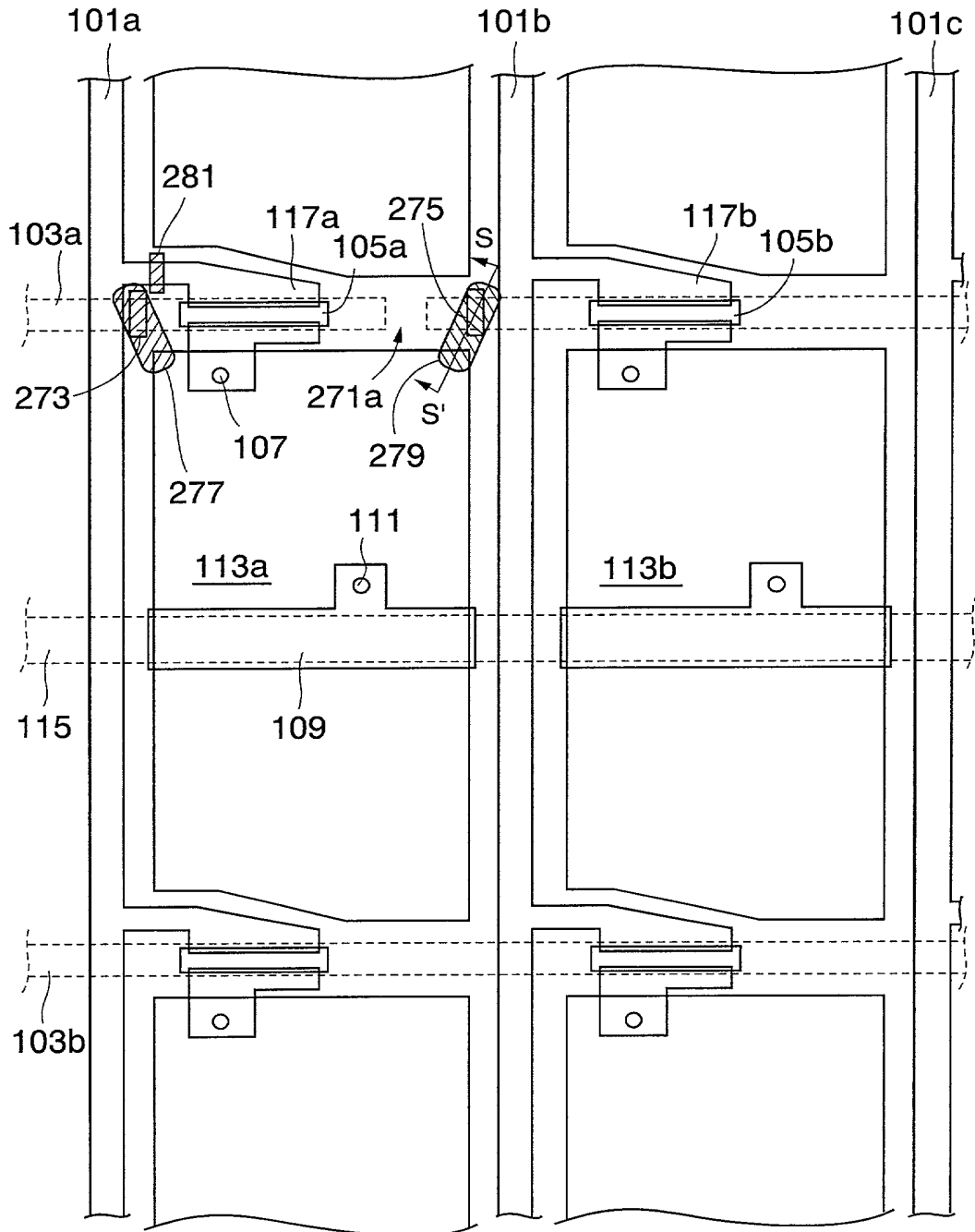




FIG.22A

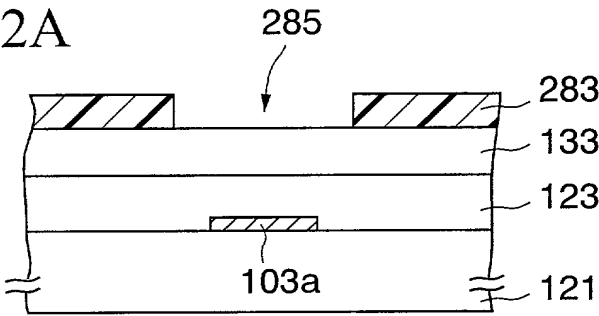


FIG.22B

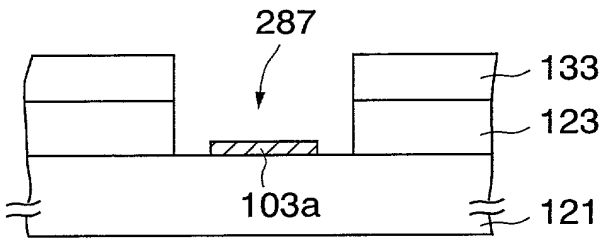


FIG.22C

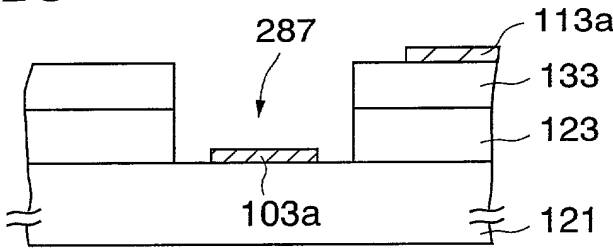


FIG.22D

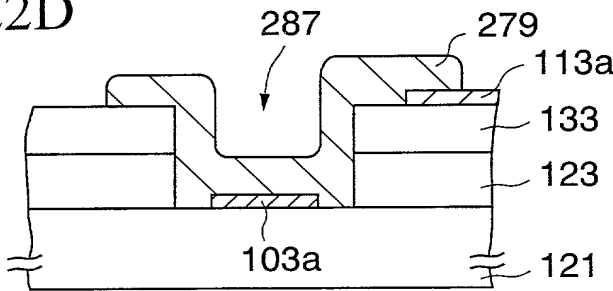


FIG.23

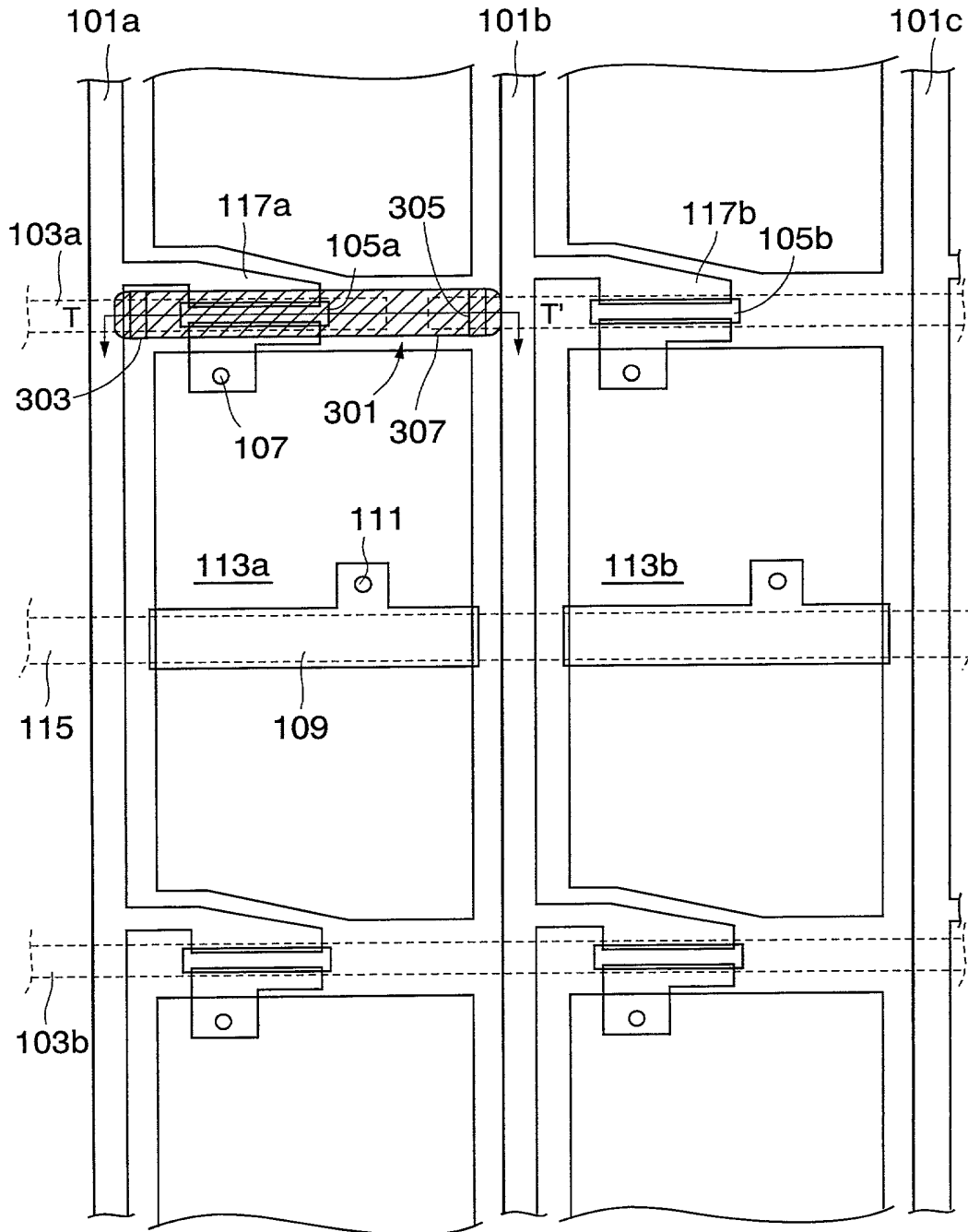


FIG.24A

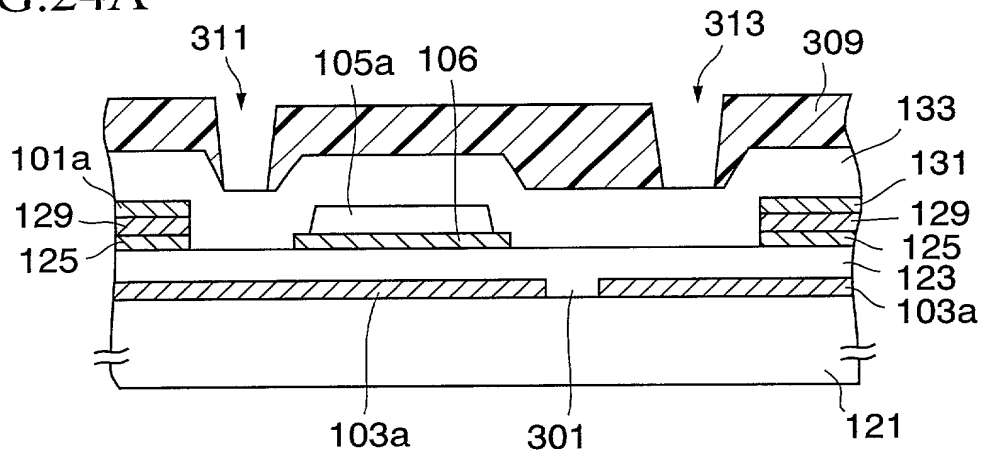


FIG.24B

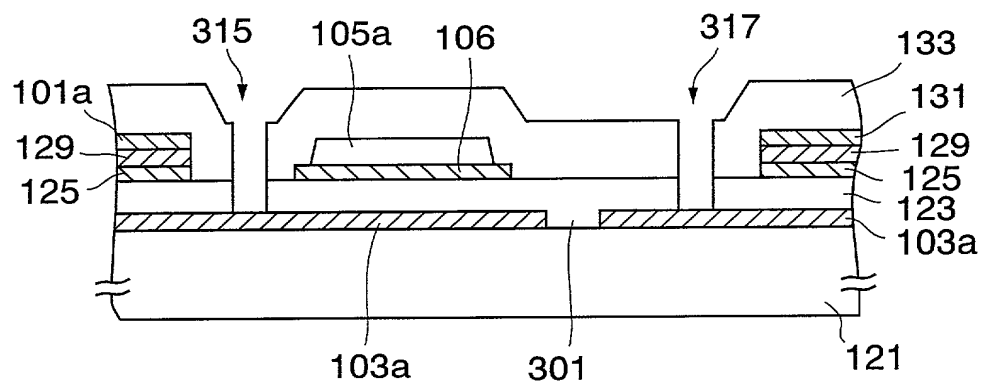


FIG.24C

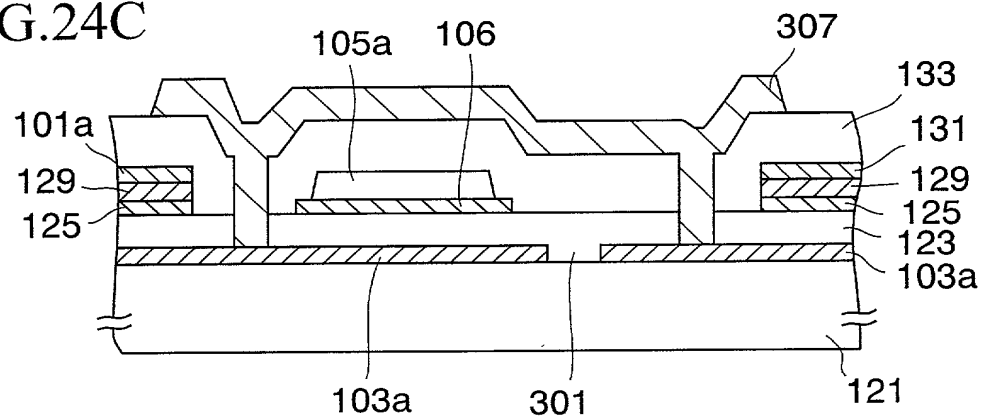


FIG.25

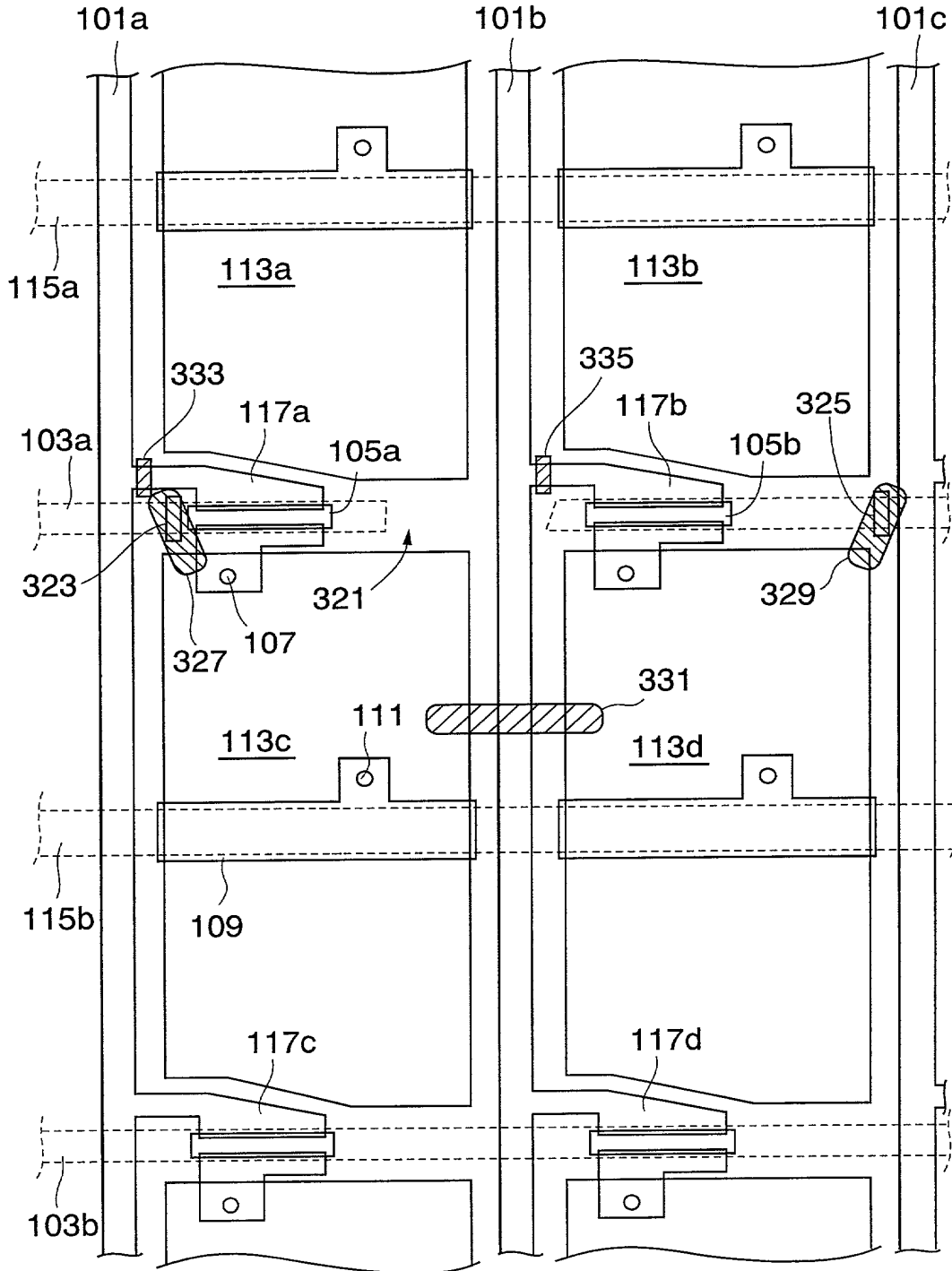


FIG.26

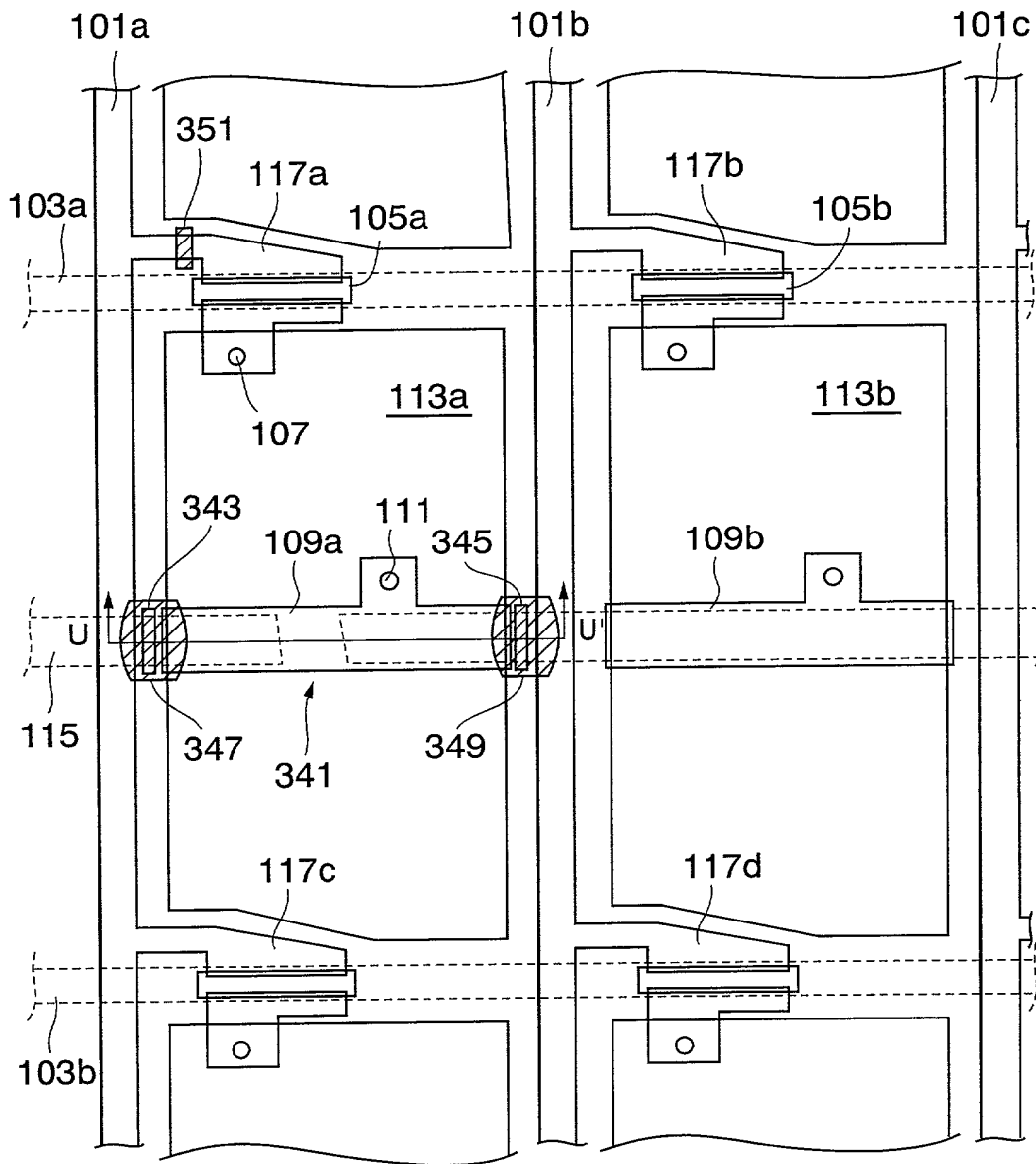


FIG.27A

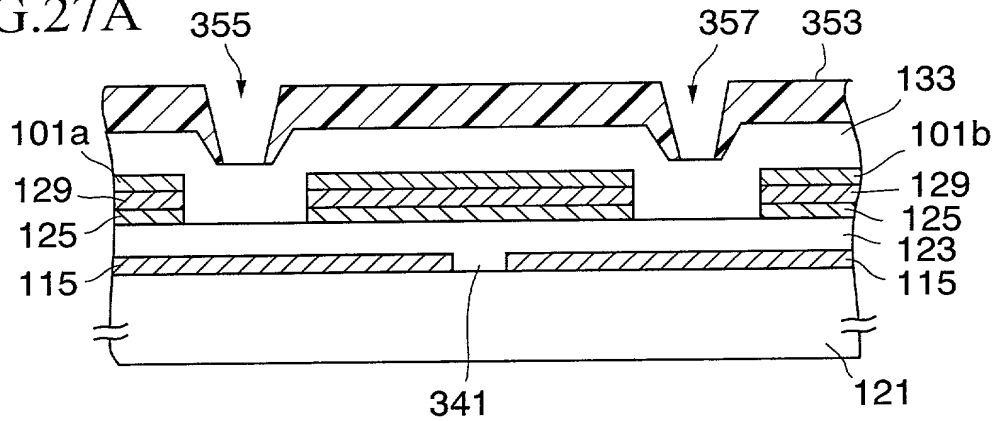


FIG.27B

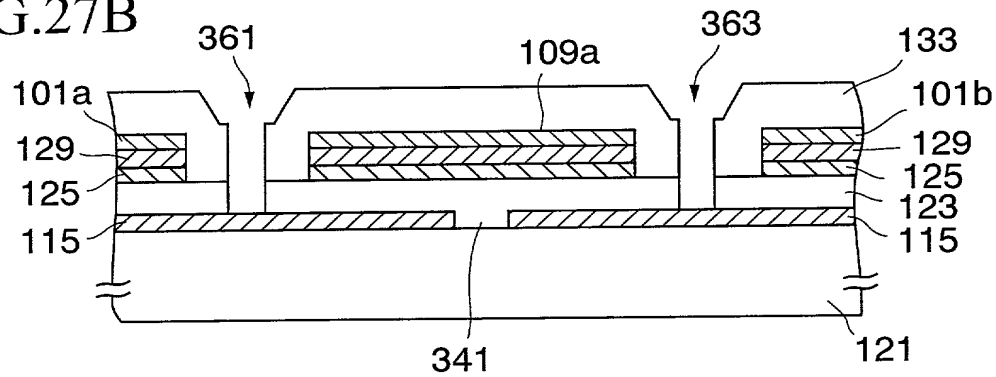


FIG.27C

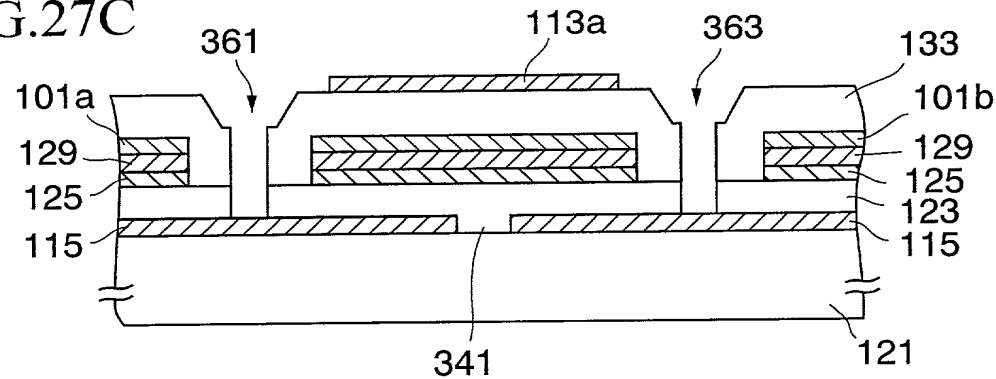


FIG.27D

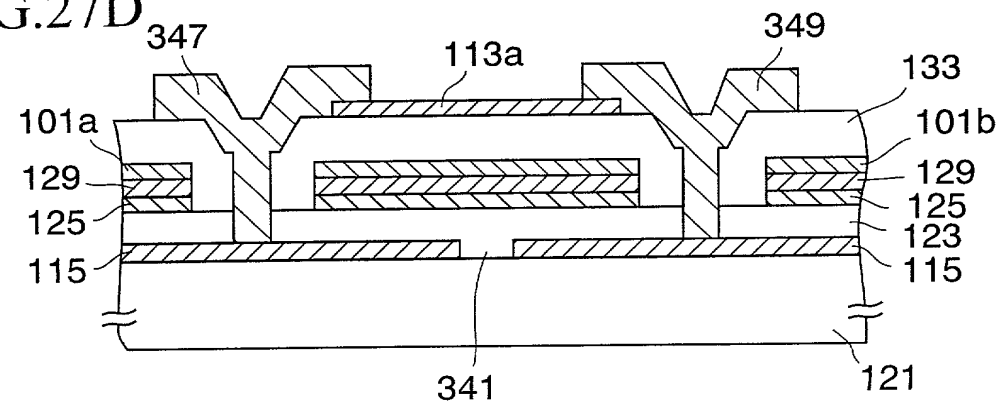
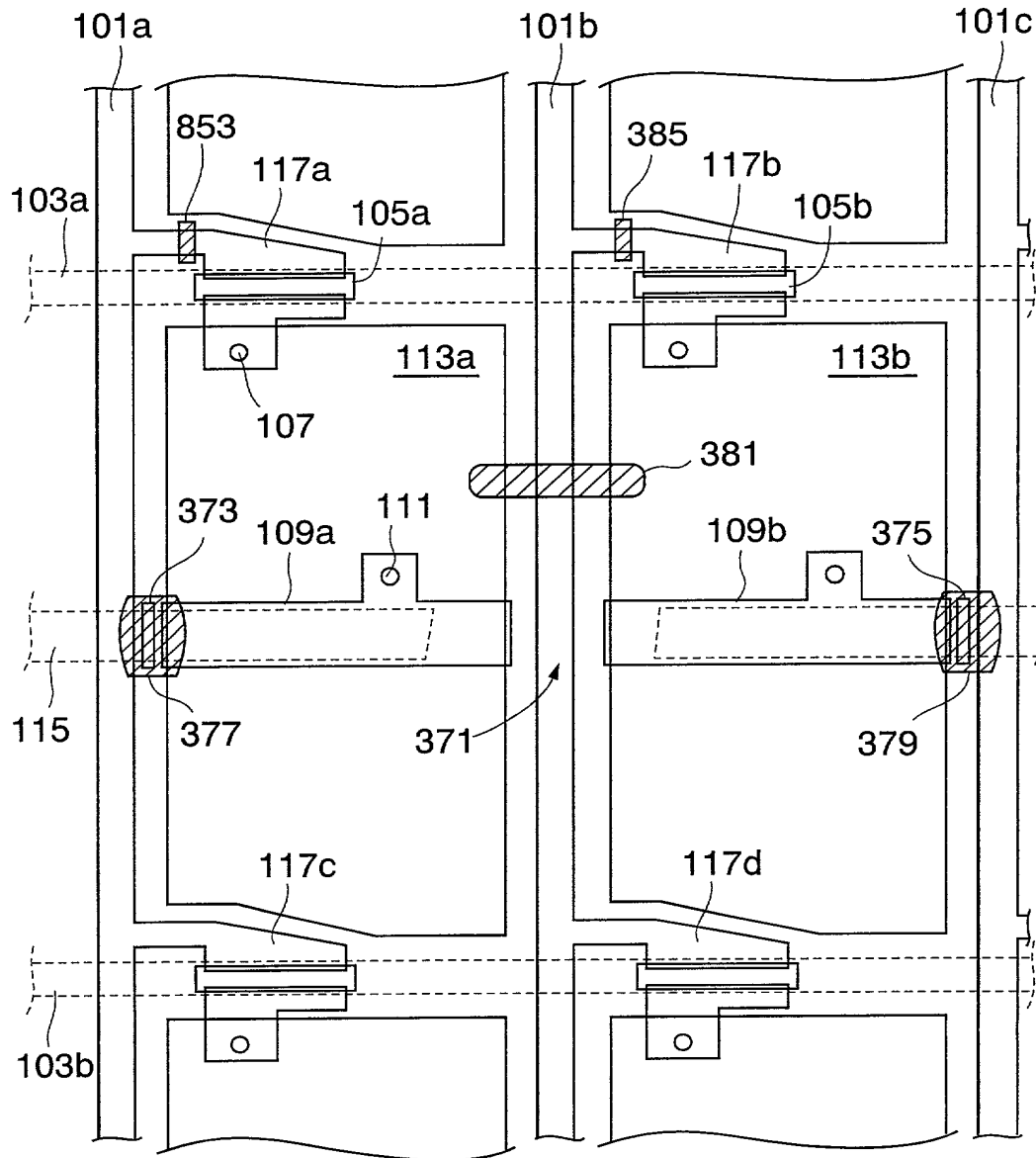


FIG.28



This diagram shows a cross-sectional view of a pixel structure. At the top, a row of rectangular gate electrodes is labeled 395. Below them, a series of vertical lines represent the pixel electrodes, labeled 391. A central pixel electrode is labeled 405. A hatched rectangular region, labeled 413, is positioned between the gate electrodes and the pixel electrodes. Arrows labeled  $V$  and  $V'$  indicate voltages applied to the gate and pixel electrodes, respectively. A label 393 points to the top surface of the pixel electrodes, and 397 points to the top surface of the hatched region. A label 415 points to the side surface of the hatched region. A horizontal line with a downward arrow is labeled "display area".



FIG.30A

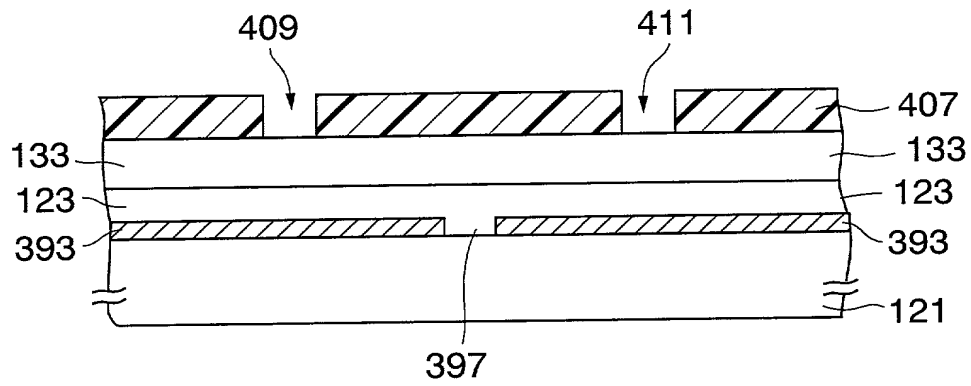


FIG.30B

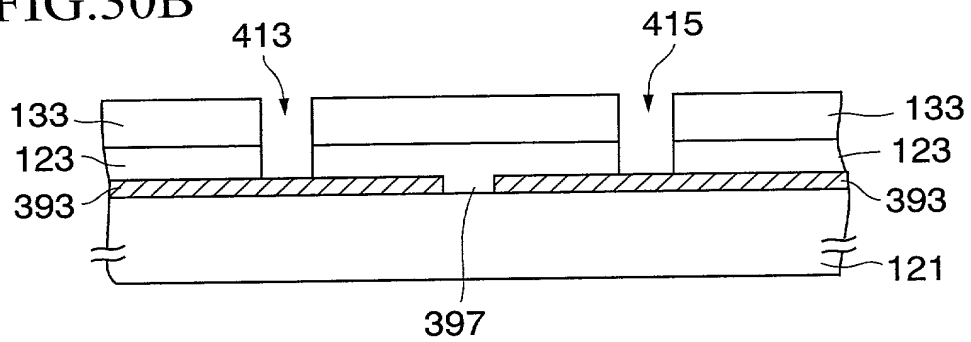
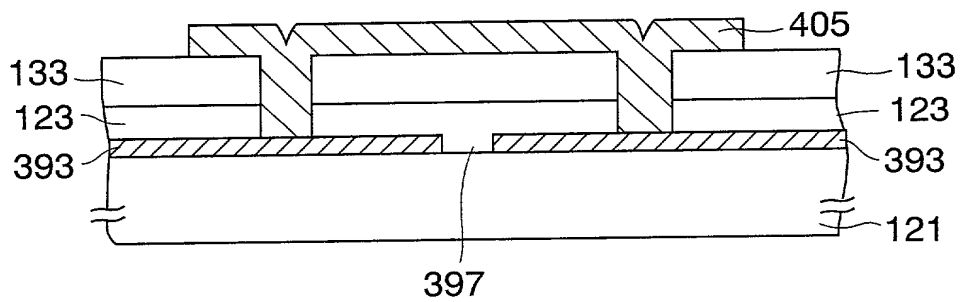


FIG.30C



[illegible]

FIG.32A

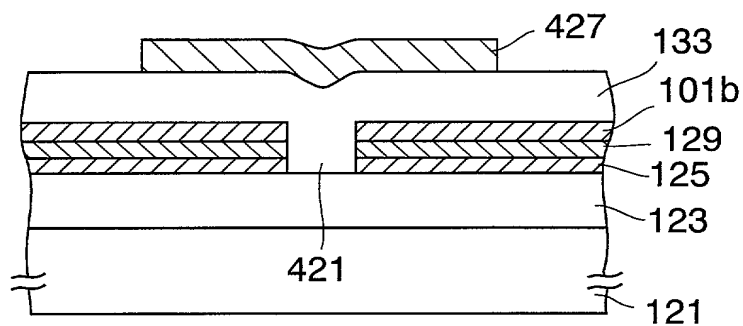


FIG.32B

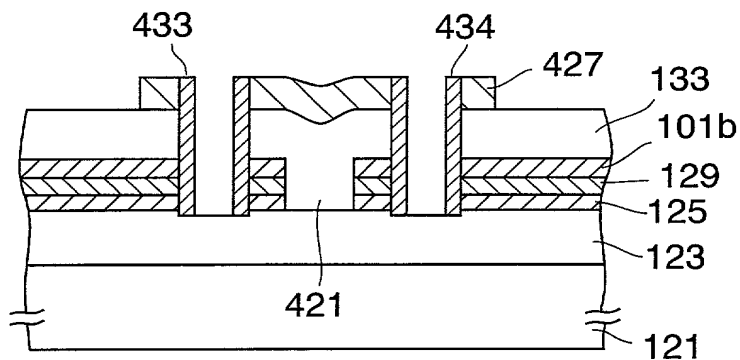


FIG.33A

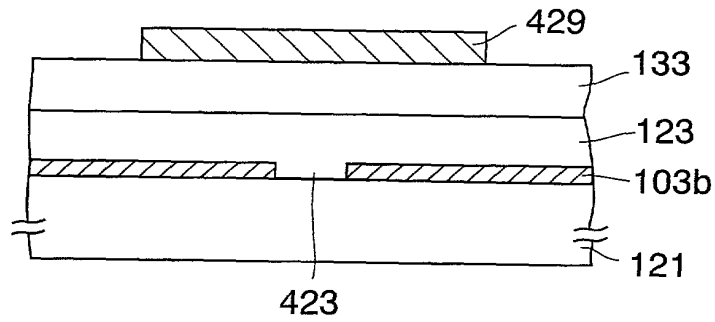


FIG.33B

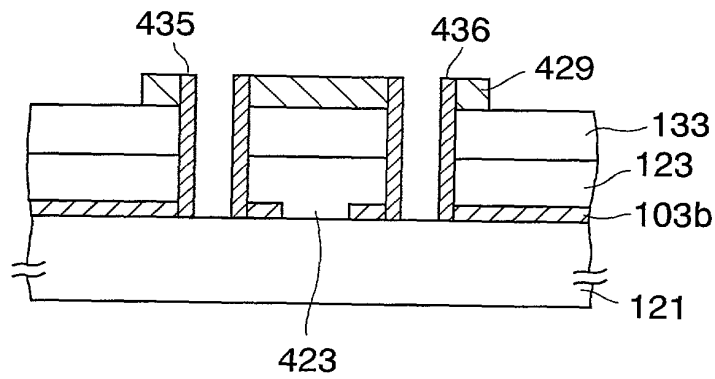


FIG.34A

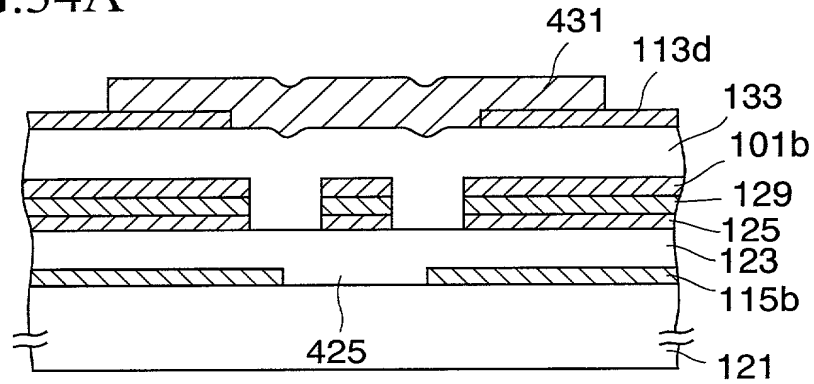


FIG.34B

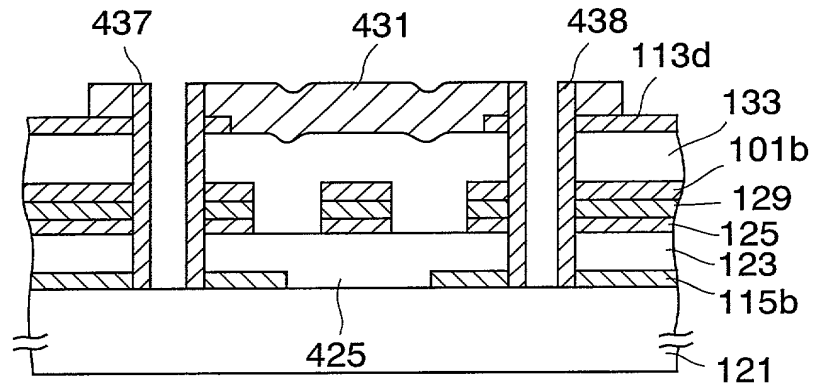


FIG.35A

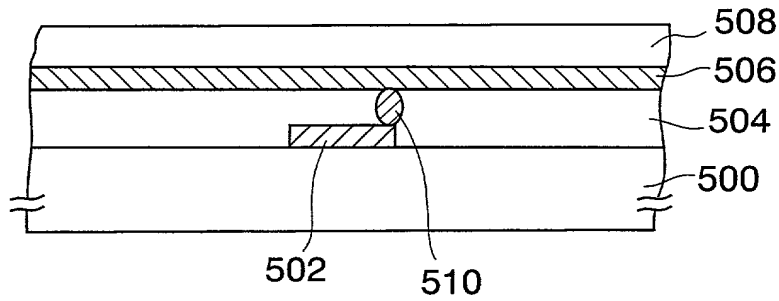


FIG.35B

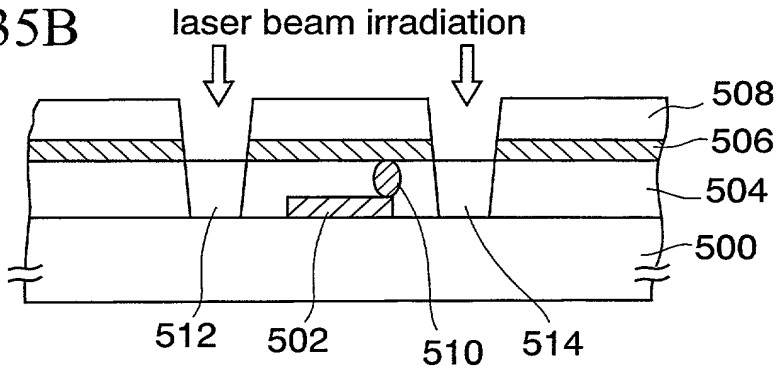


FIG.35C

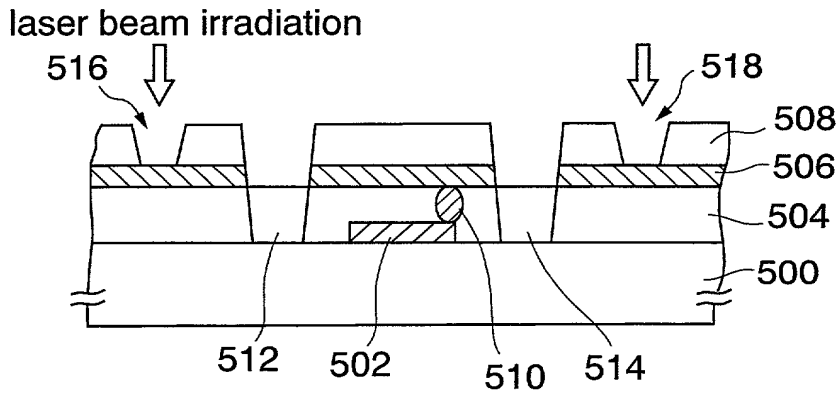


FIG.35D

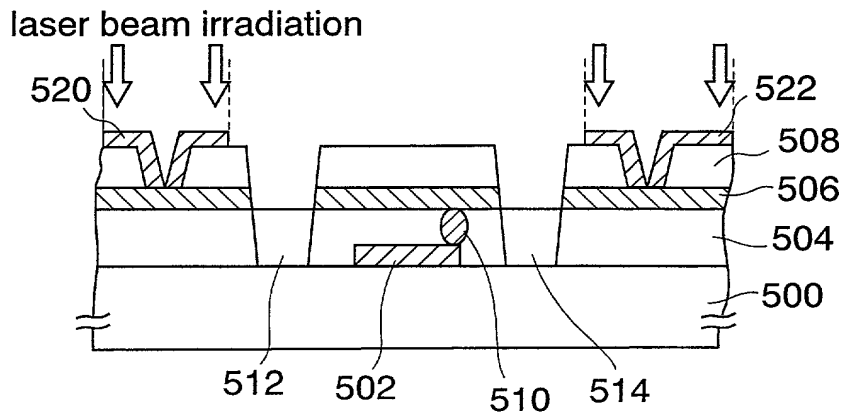


FIG.36

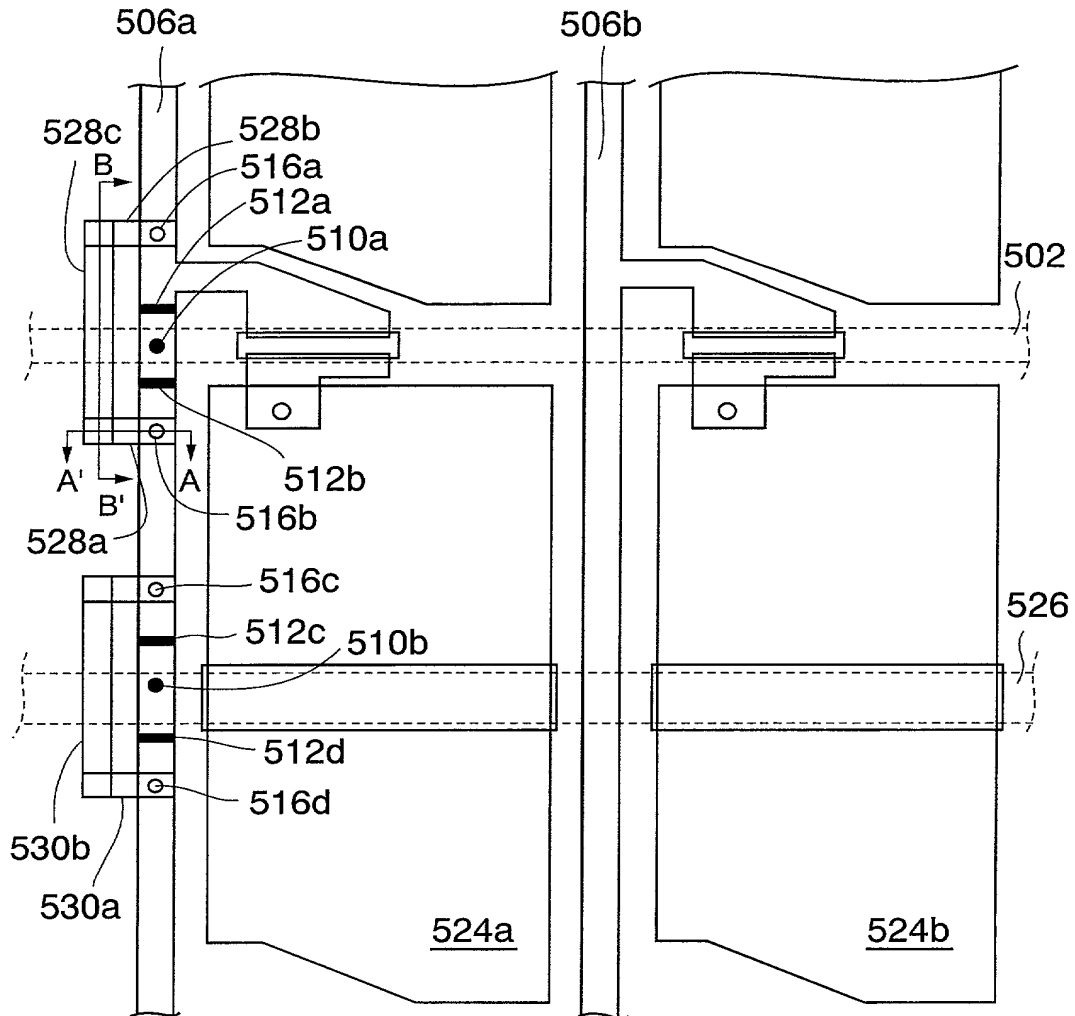


FIG.37

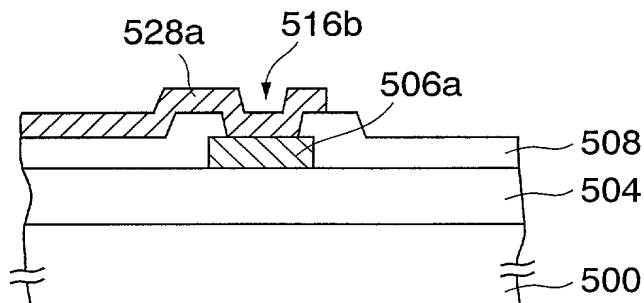


FIG.38

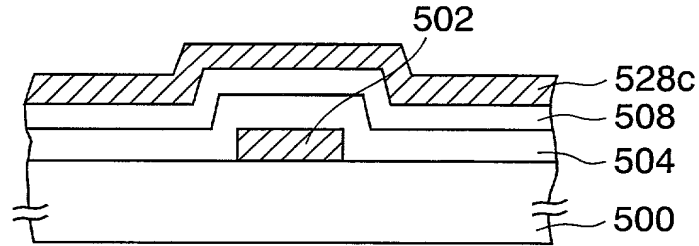


FIG.39

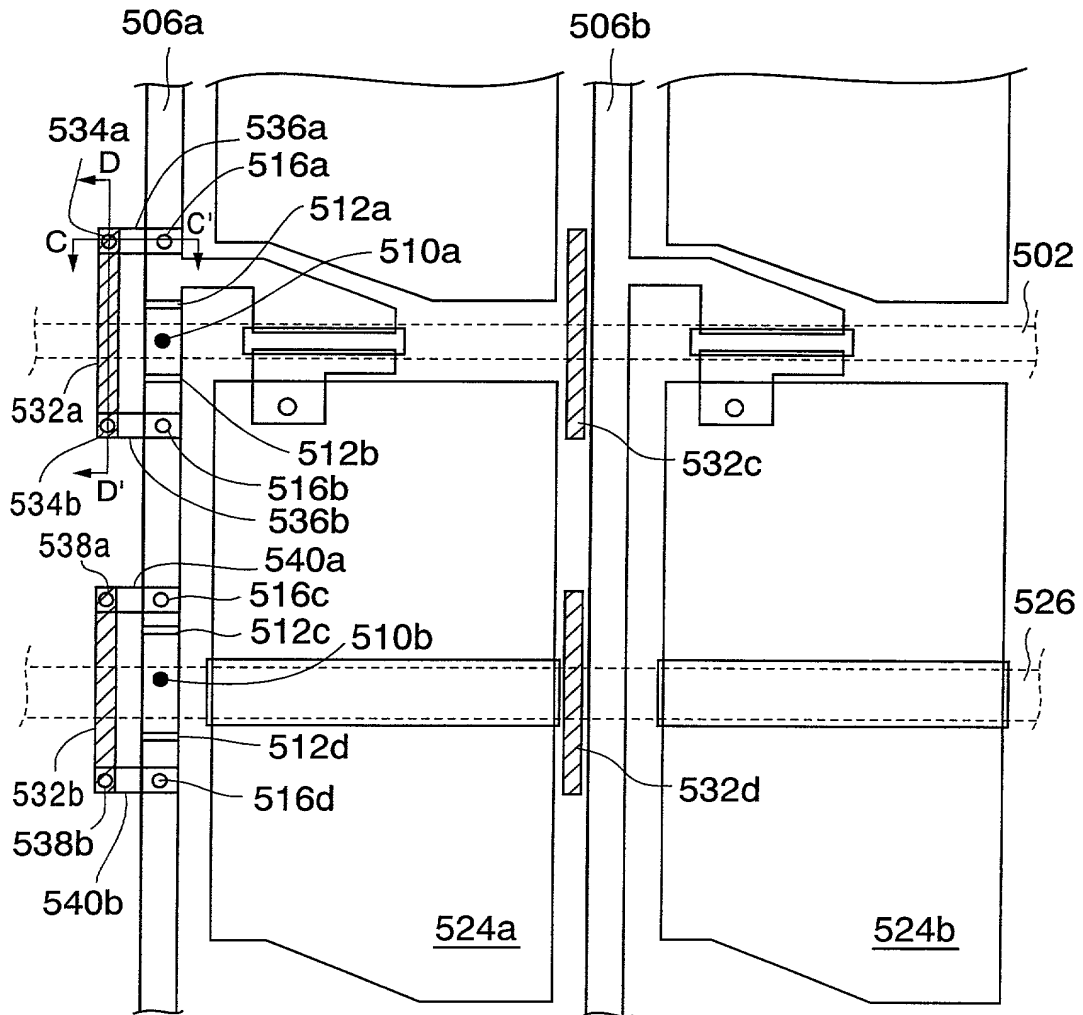




FIG.40

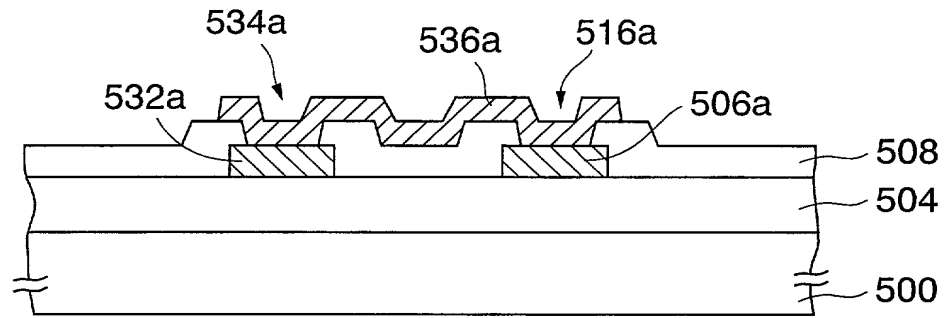


FIG.41

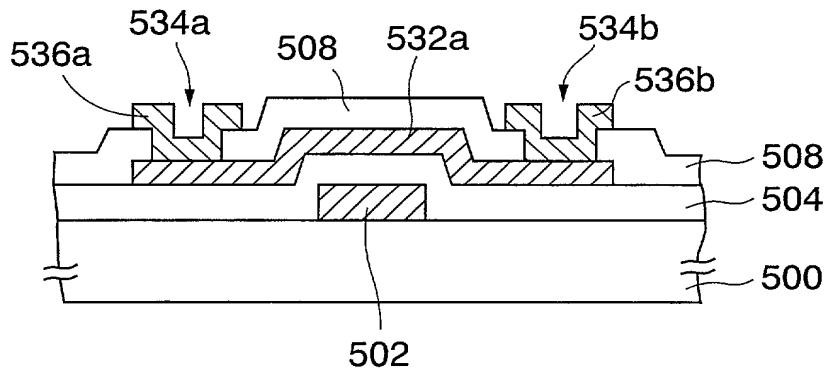


FIG.43

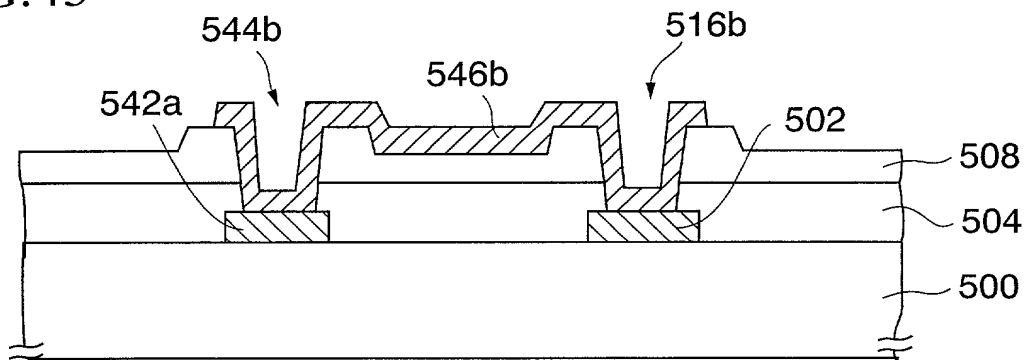


FIG.44

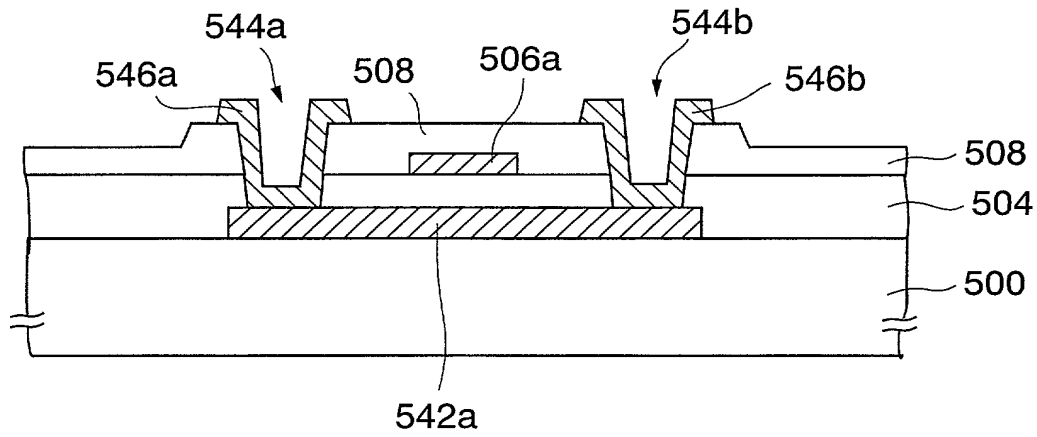


FIG.45

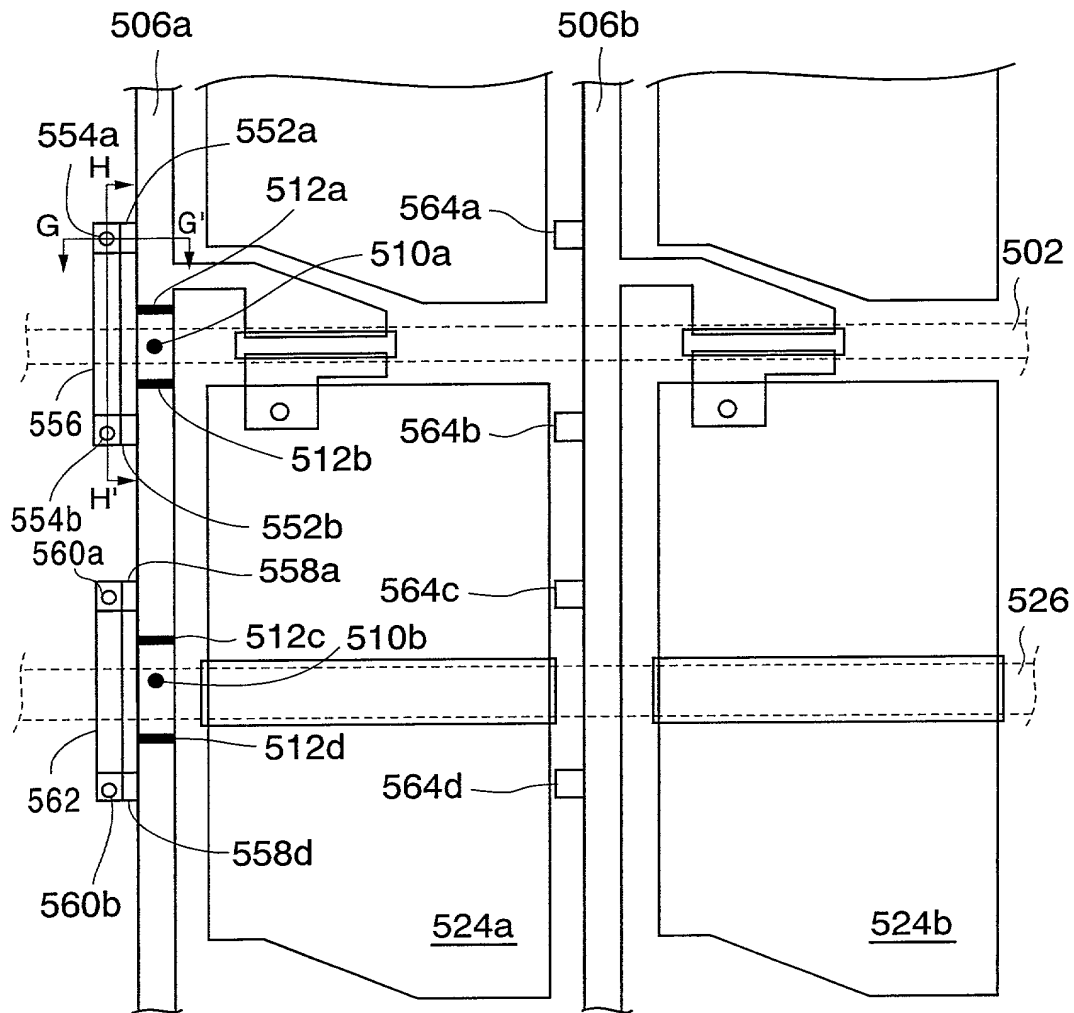


FIG.46

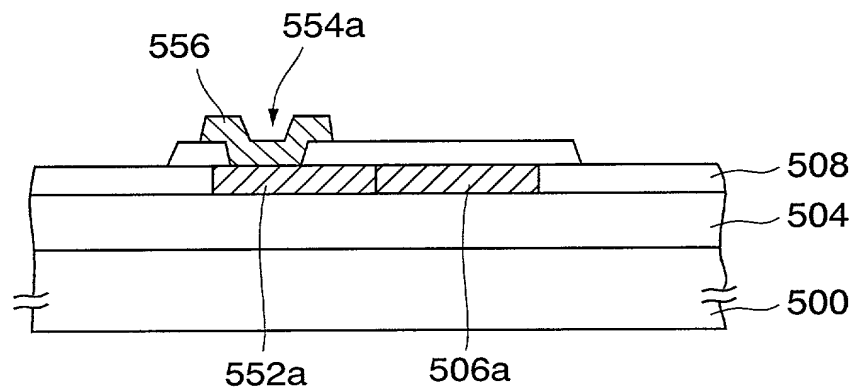


FIG.47

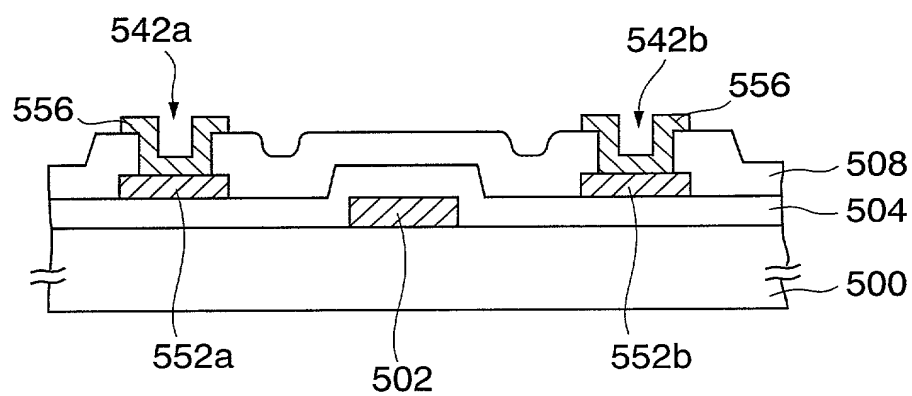


FIG.48

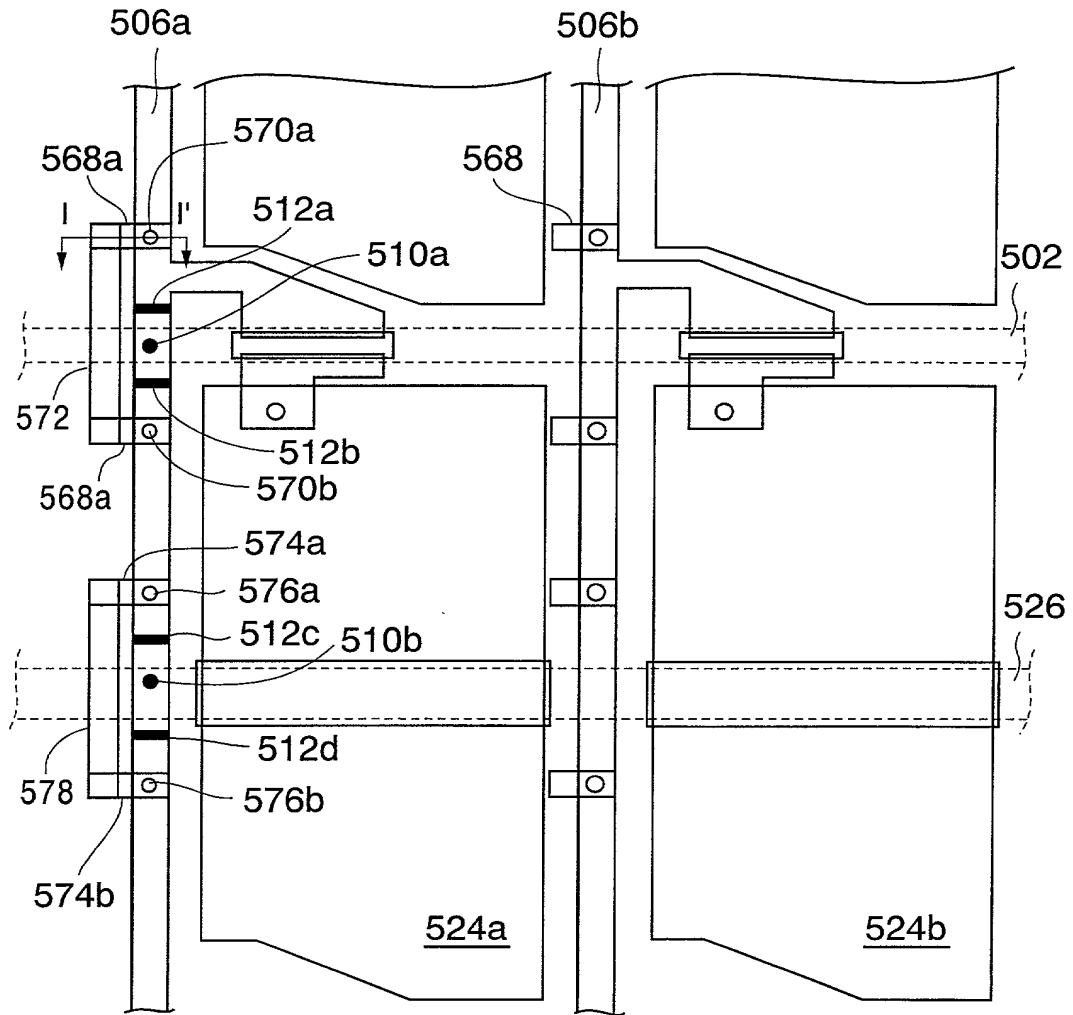


FIG.49

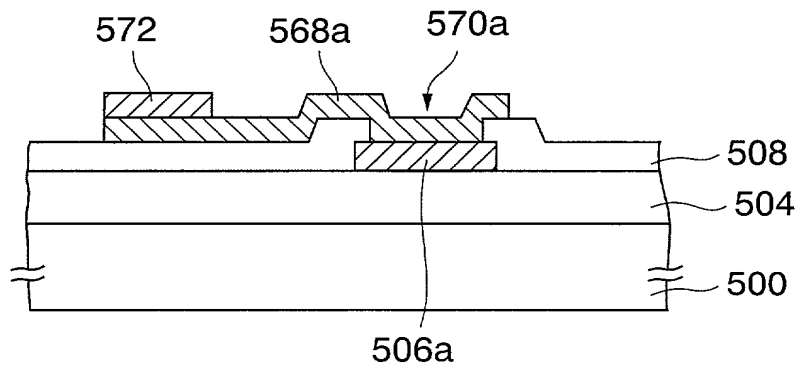


FIG.50

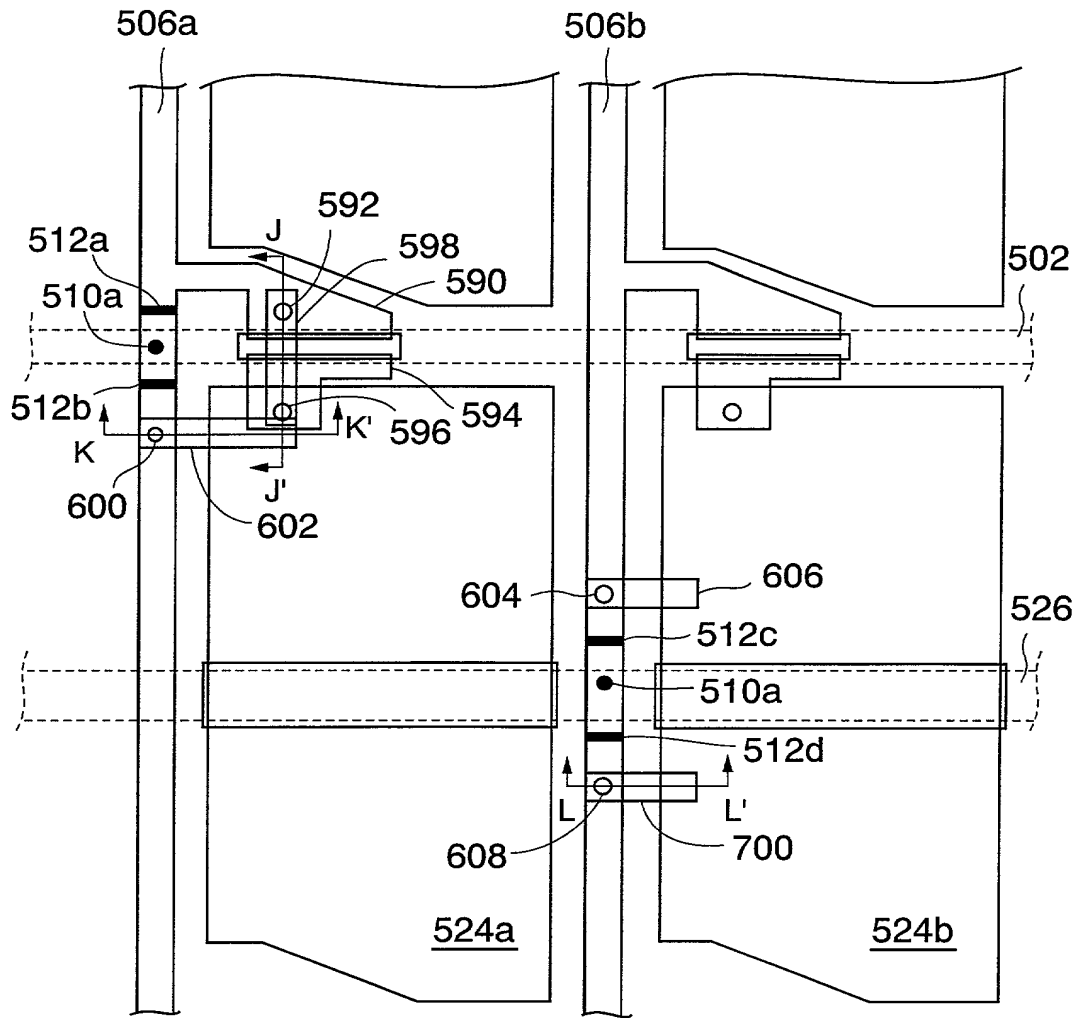


FIG.51

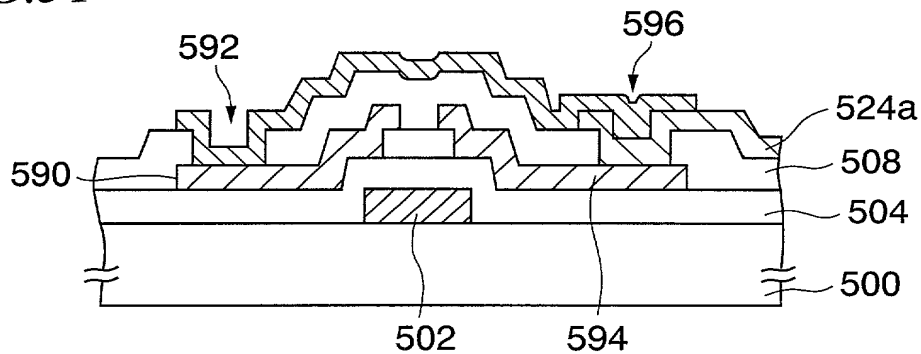


FIG.52

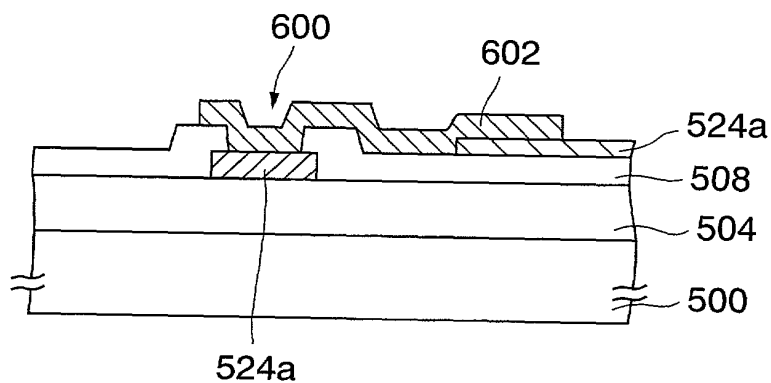


FIG.53

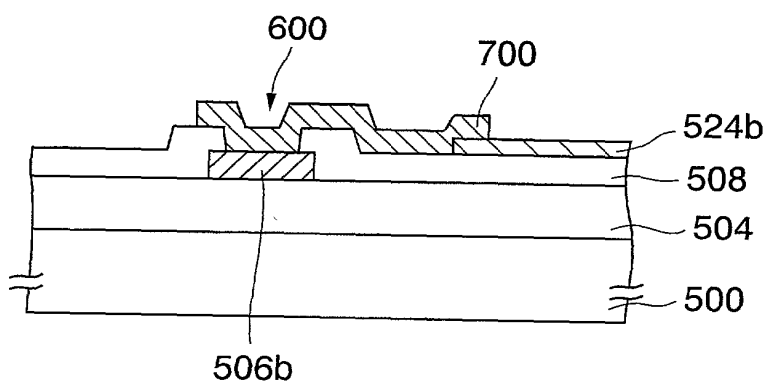




FIG.54

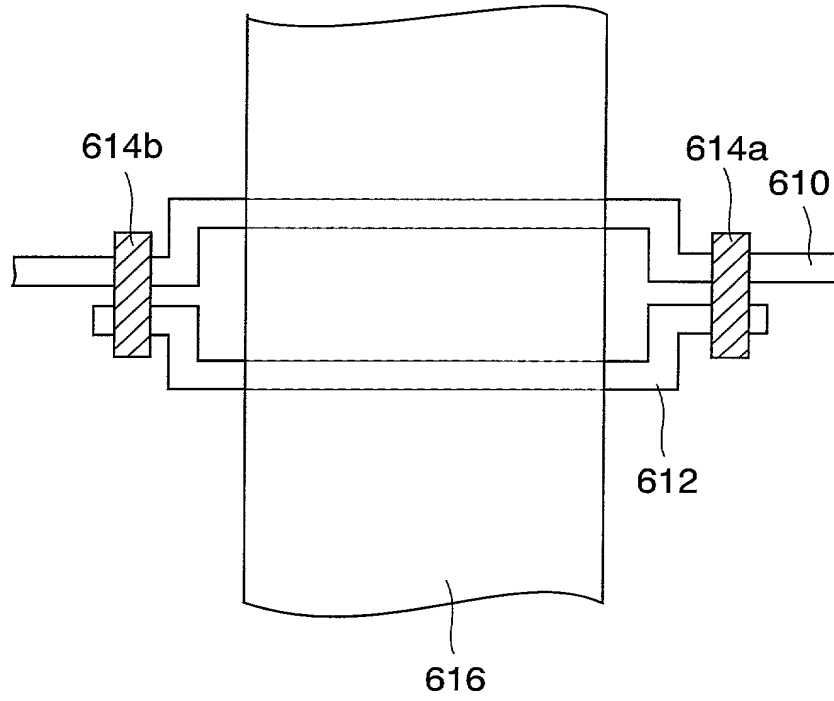


FIG.55

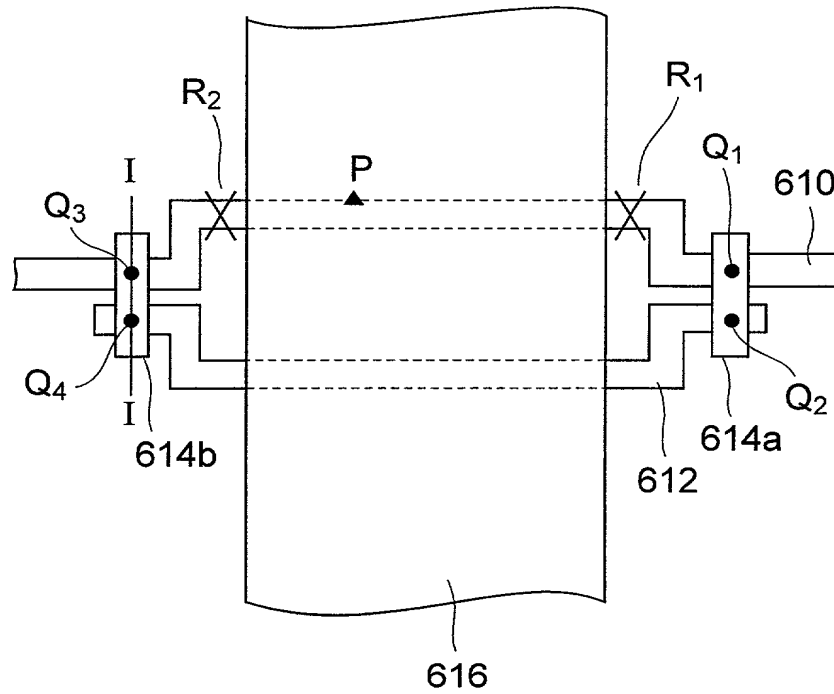


FIG.56

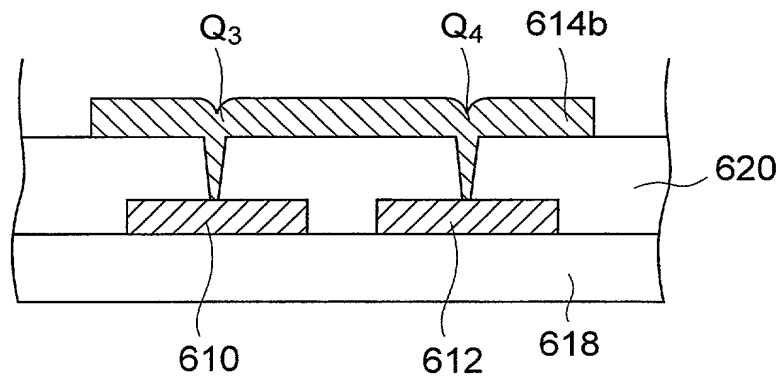


FIG.57

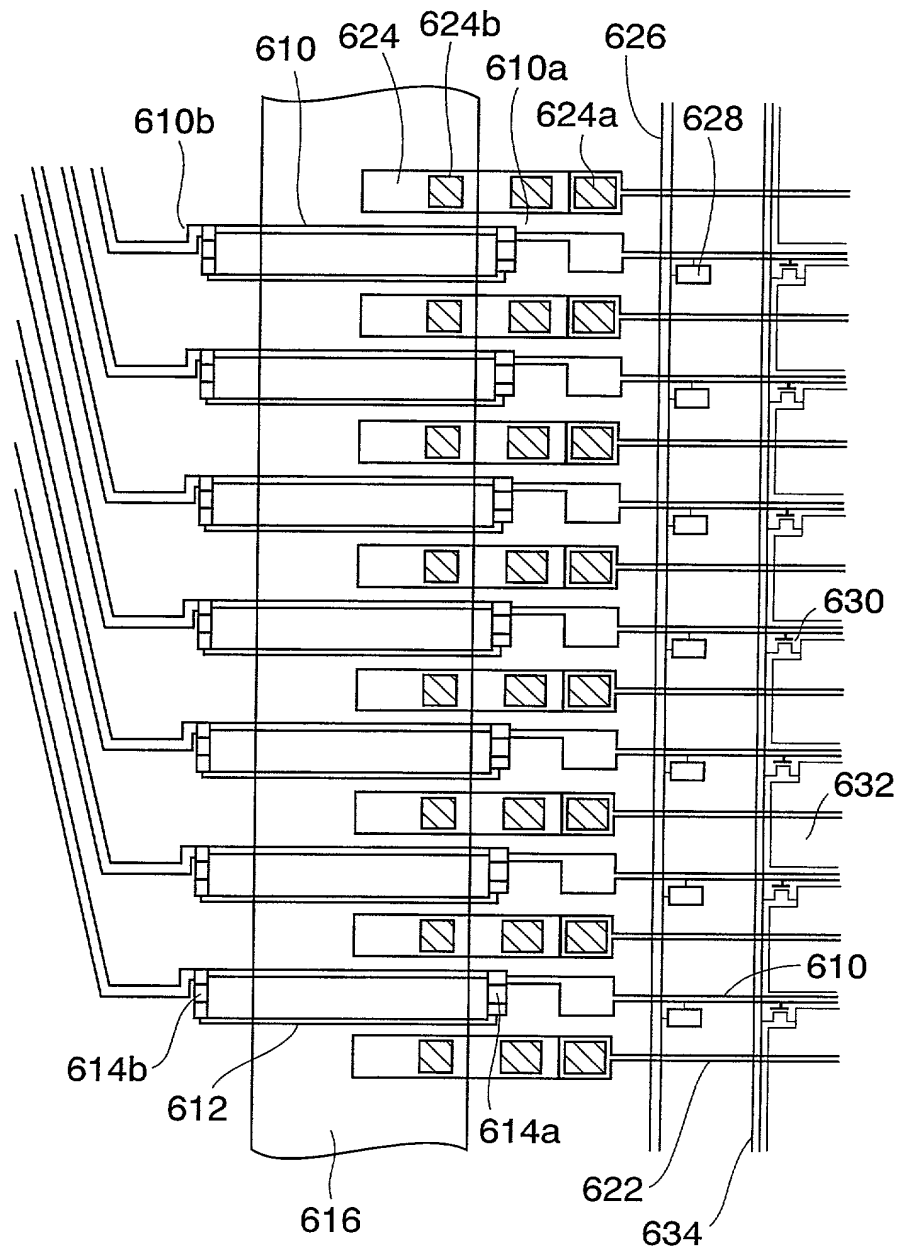


FIG.58

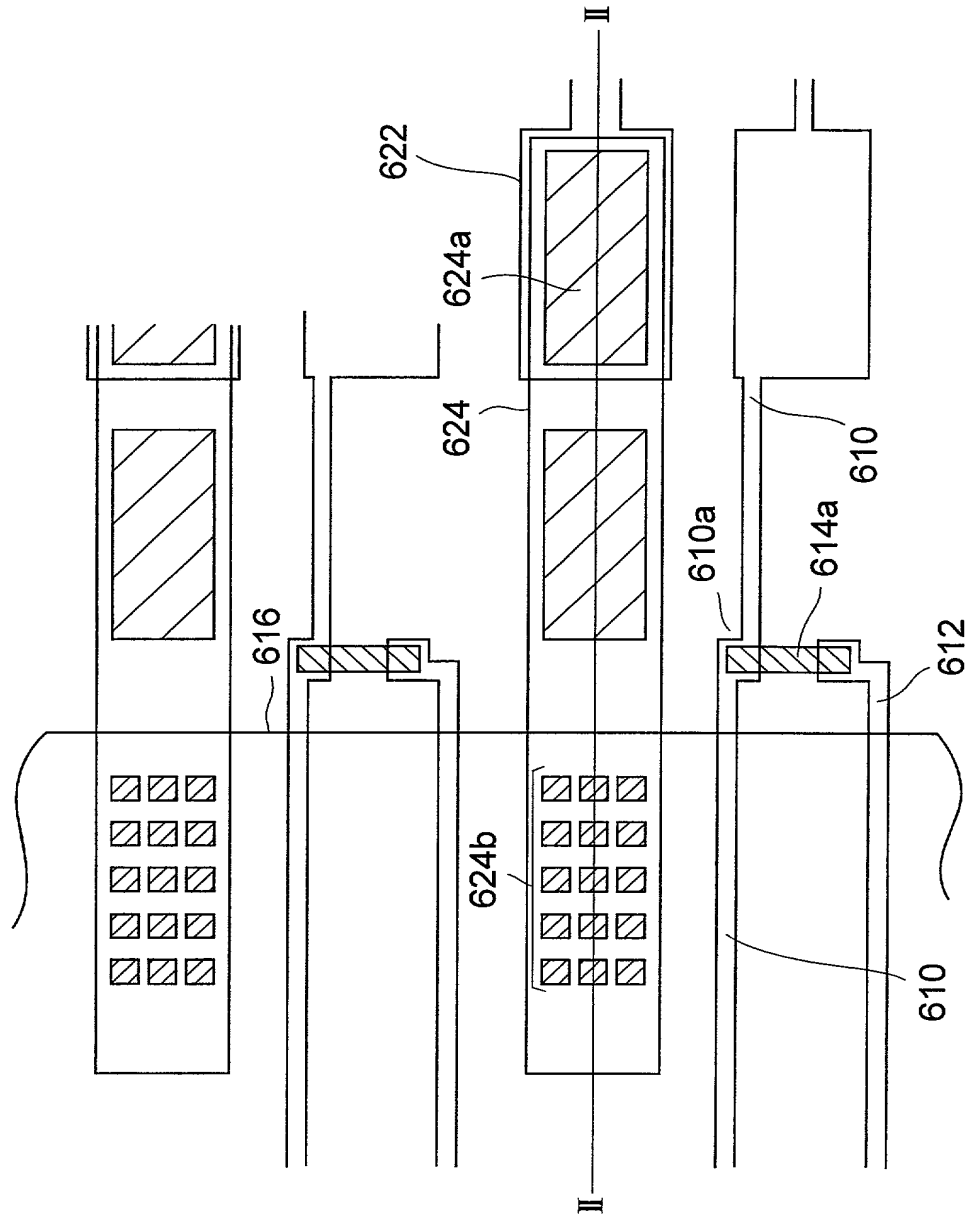


FIG.59

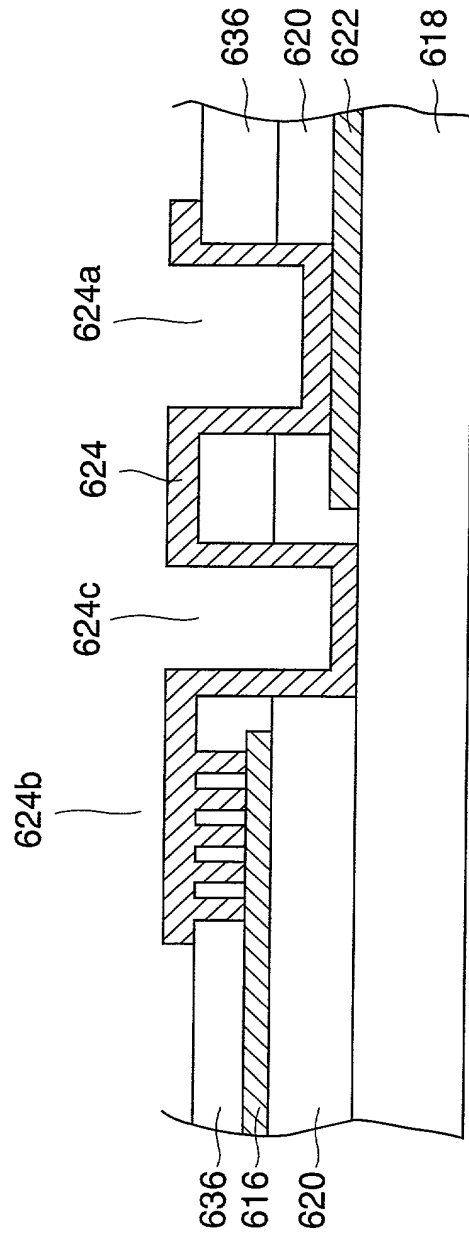


FIG.60

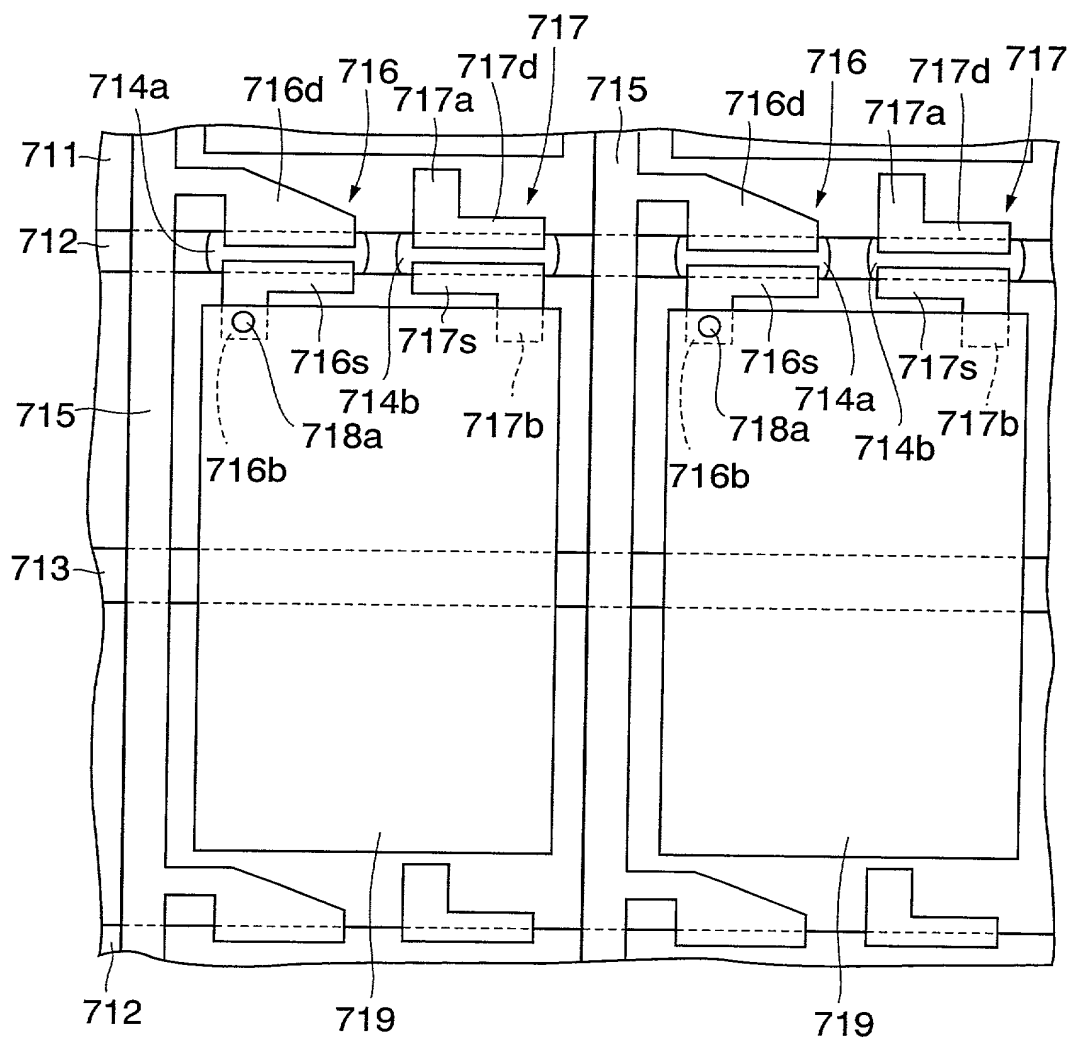


FIG.61

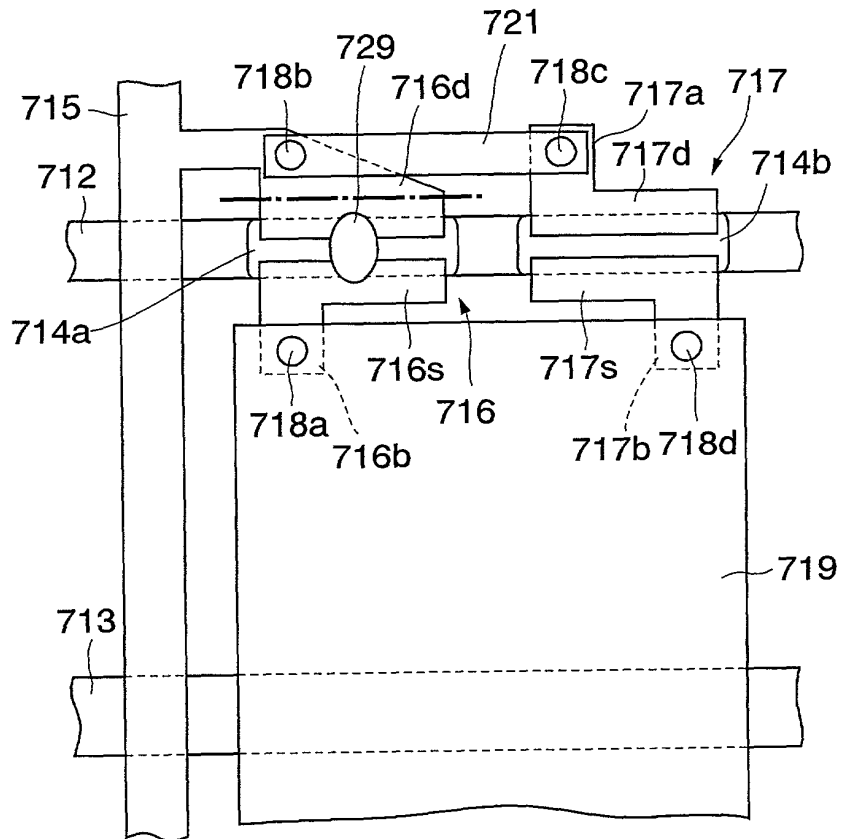


FIG.62A

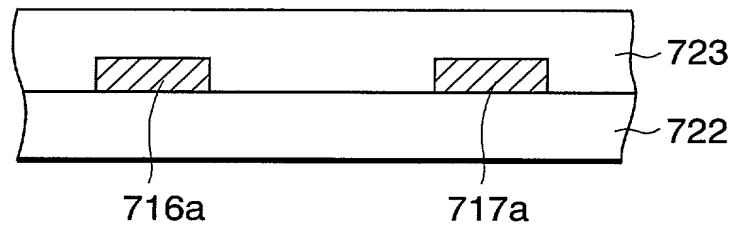


FIG.62B

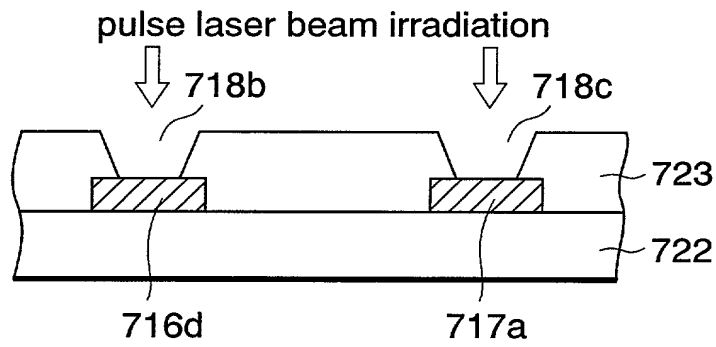


FIG.62C

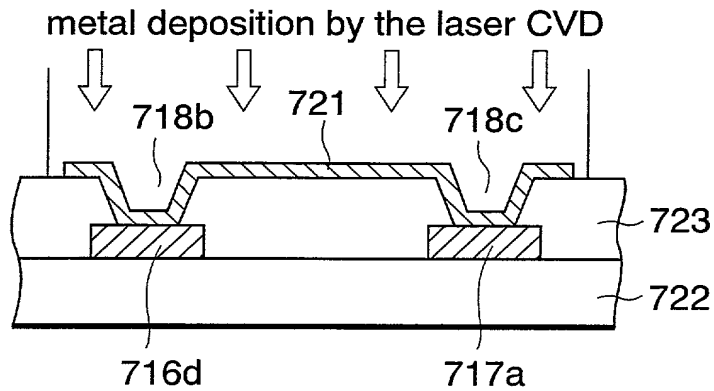




FIG.63A

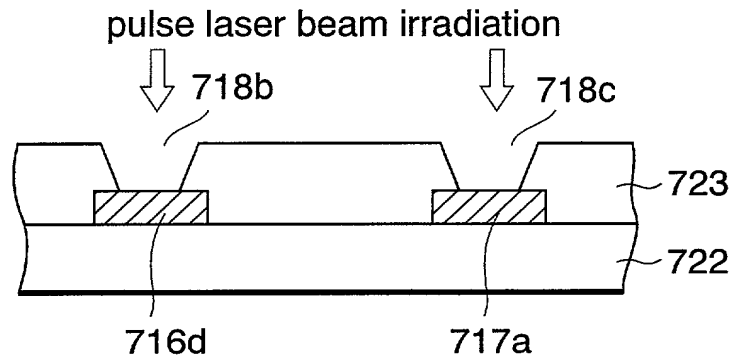


FIG.63B

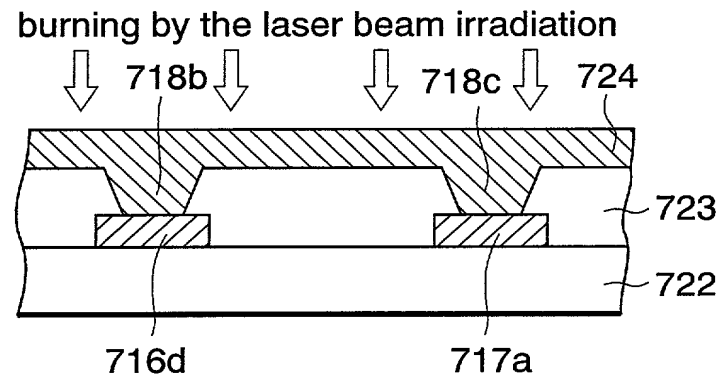
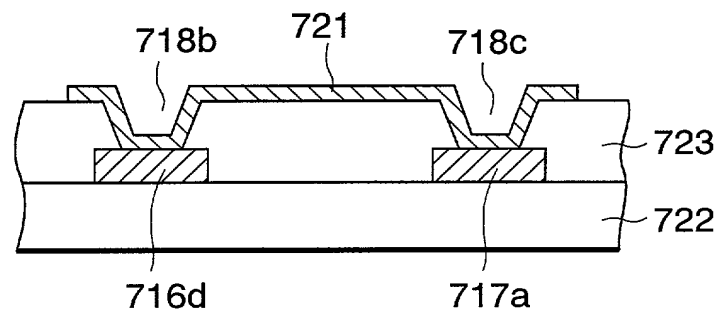


FIG.63C



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FIG.64

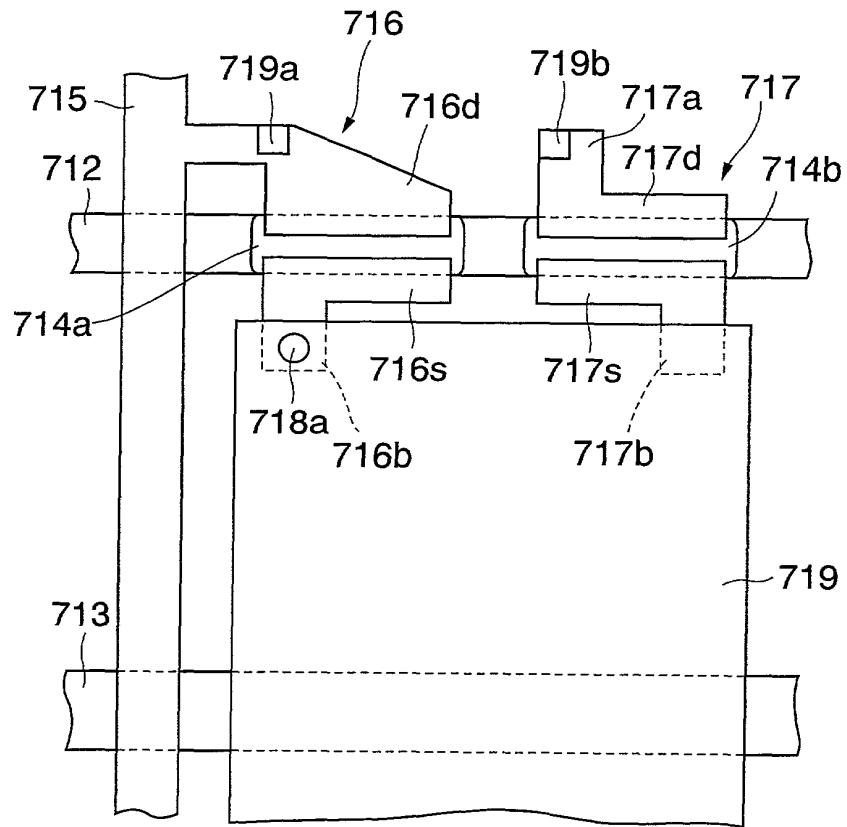


FIG.65

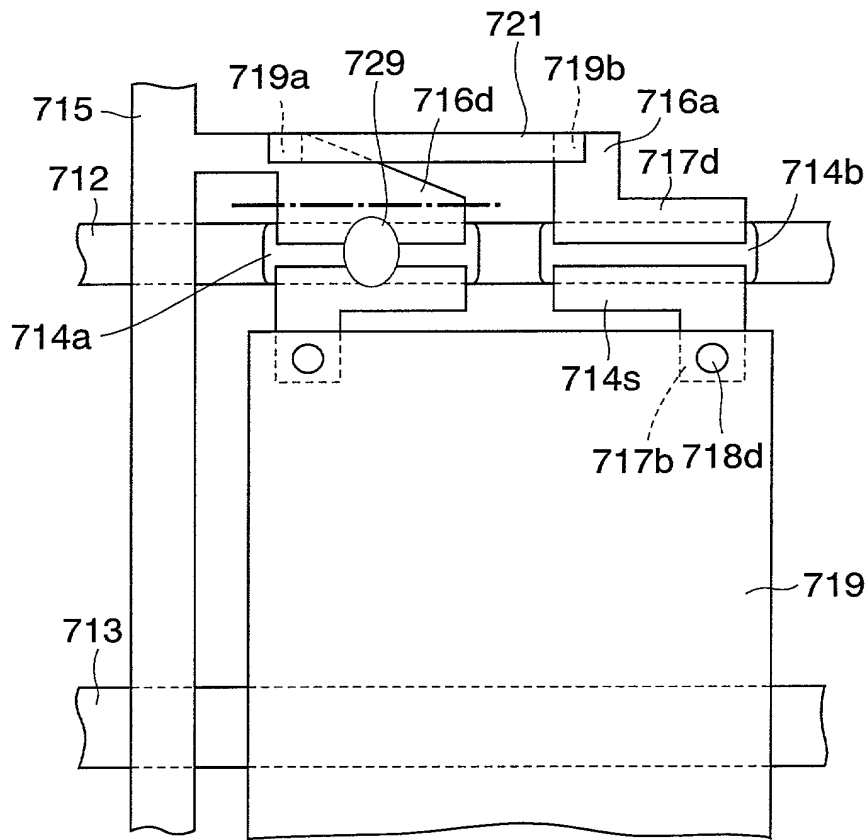


FIG.66

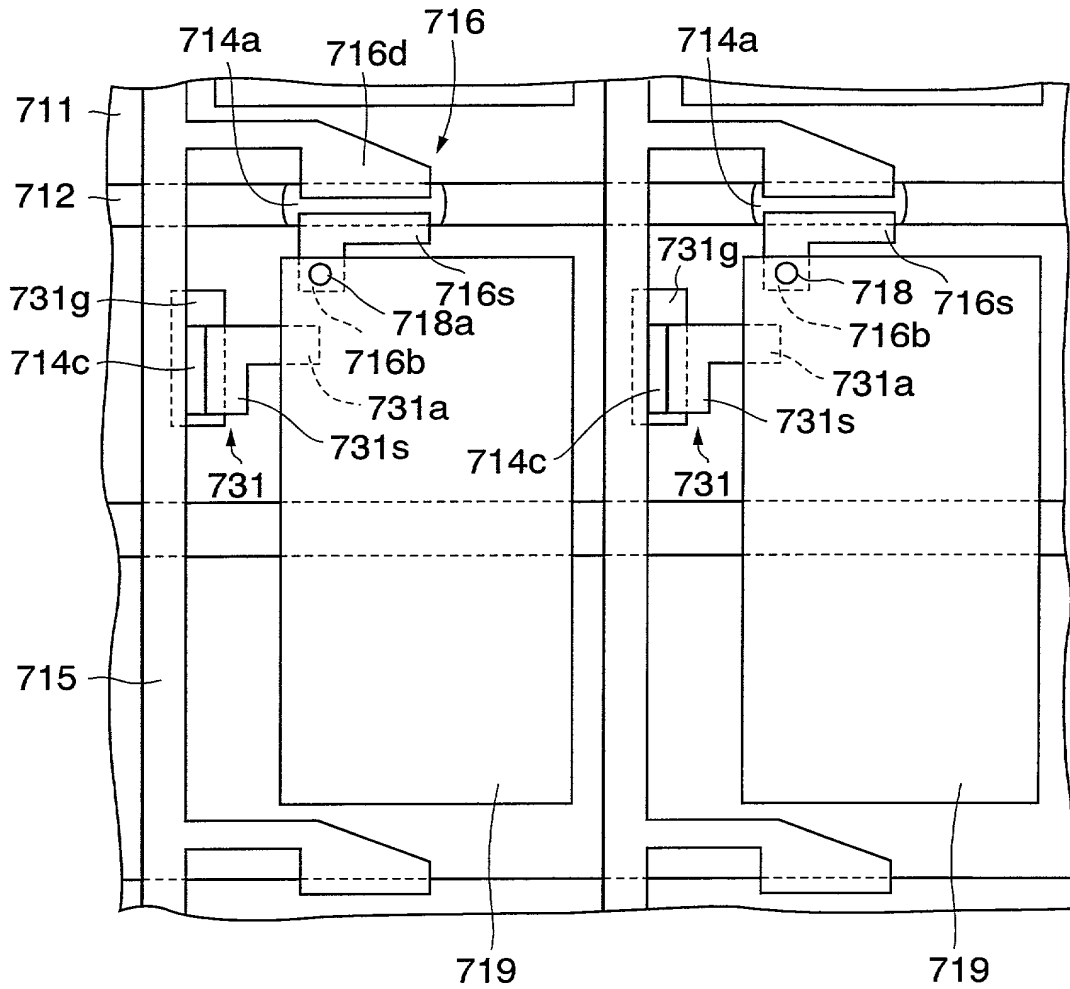


FIG.67

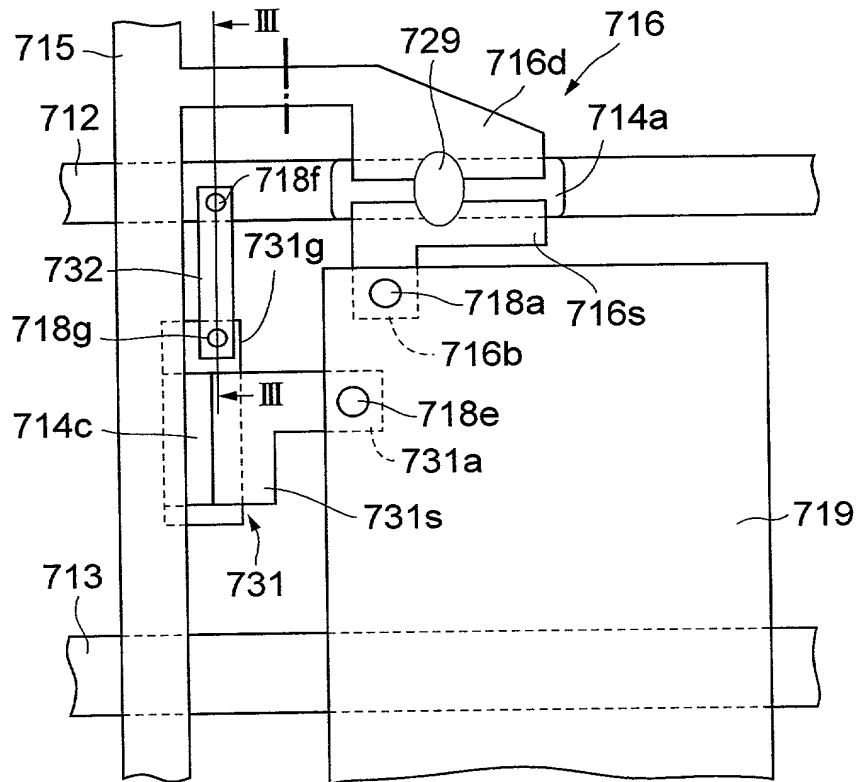


FIG.68

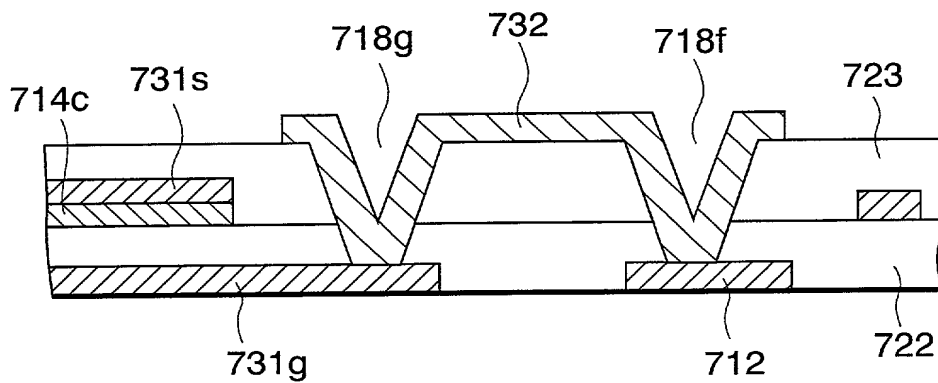




FIG.70A

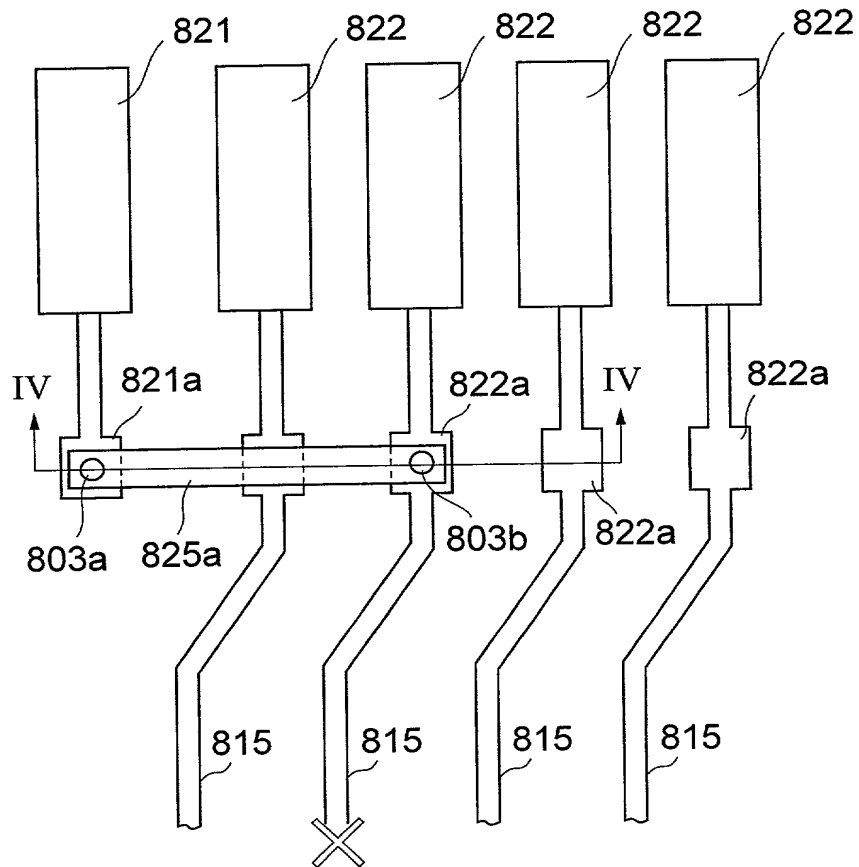


FIG.70B

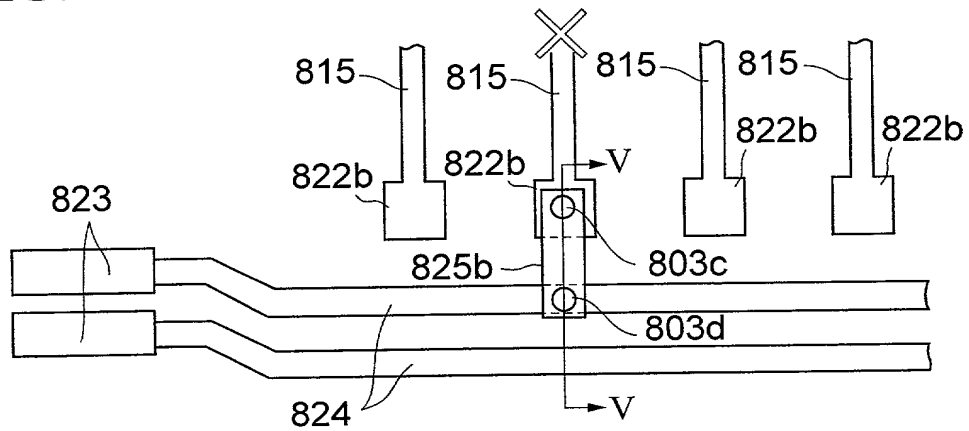




FIG.71A

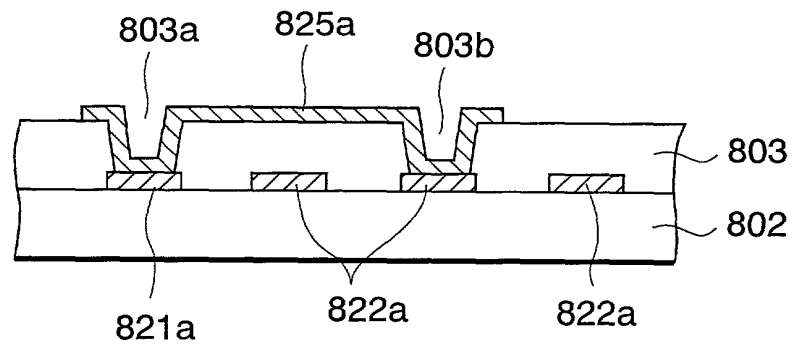


FIG.71B

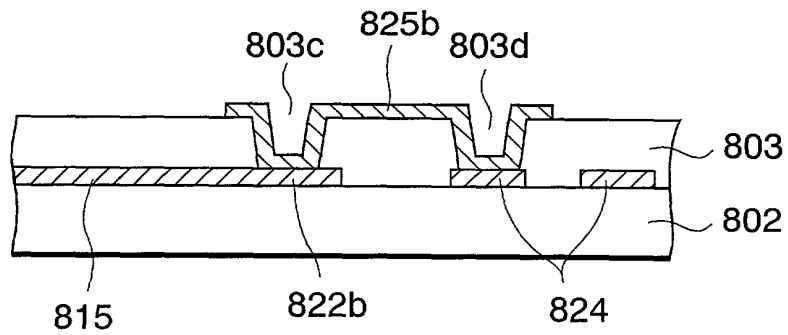


FIG.72A

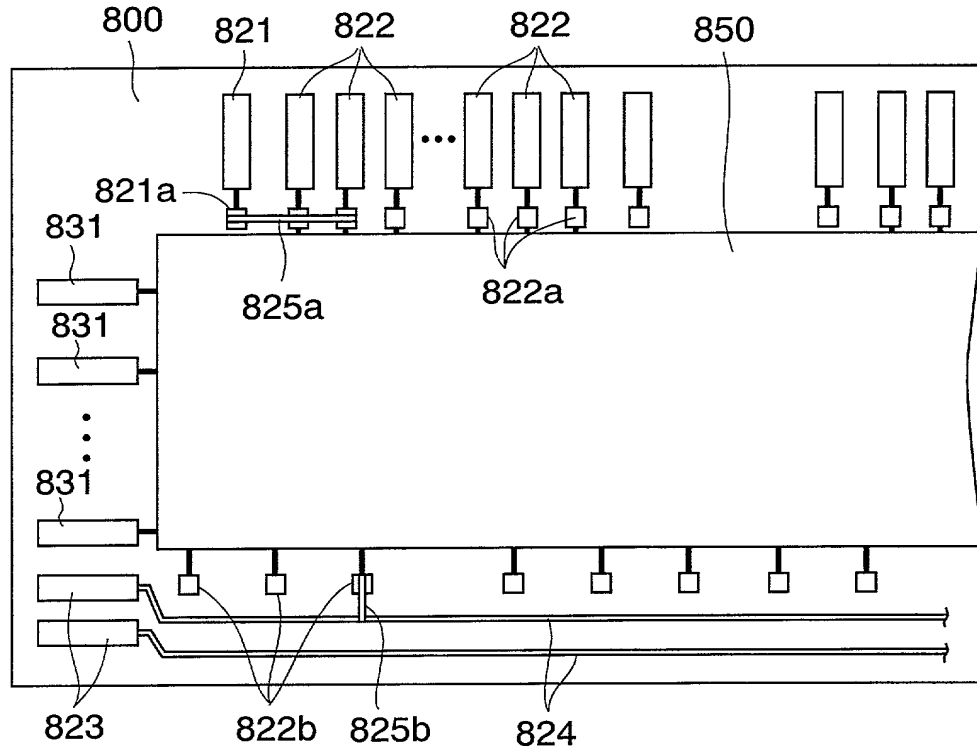


FIG.72B

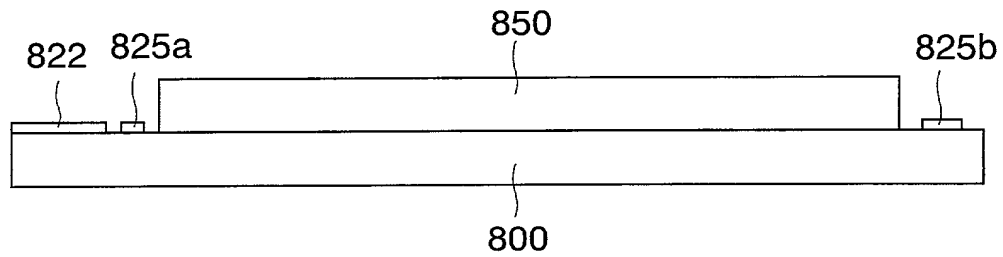


FIG.73A

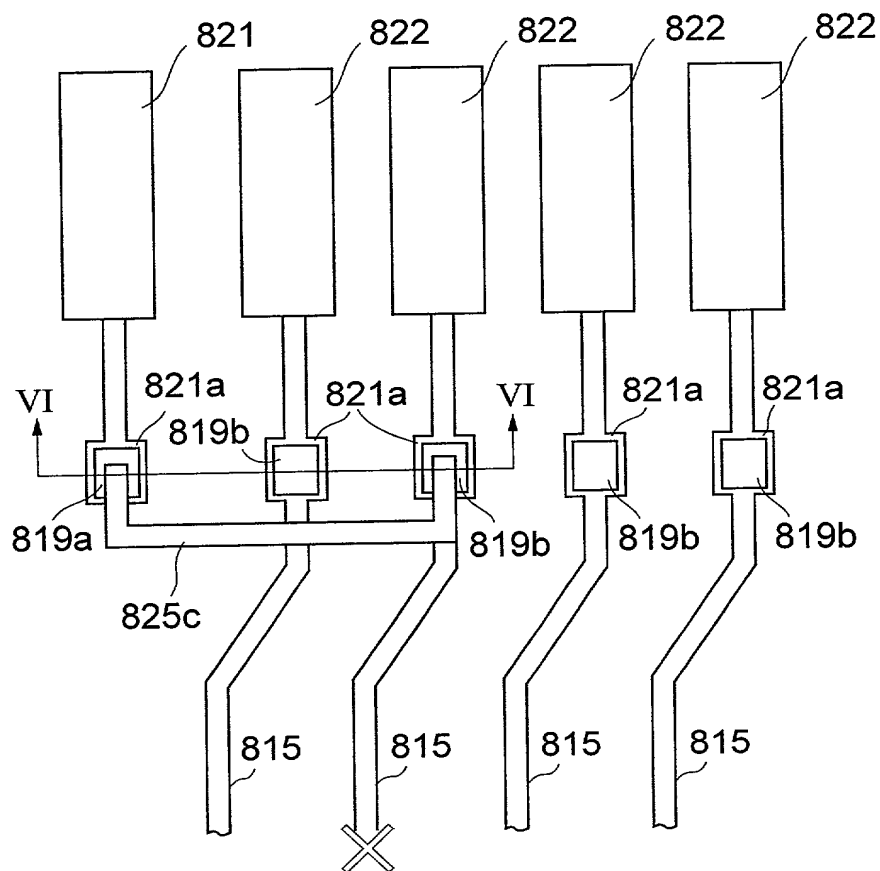


FIG.73B

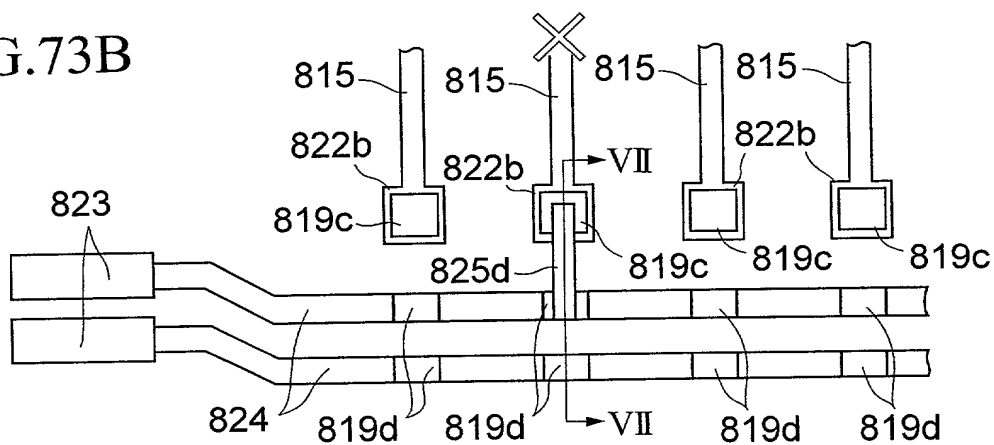


FIG.74A

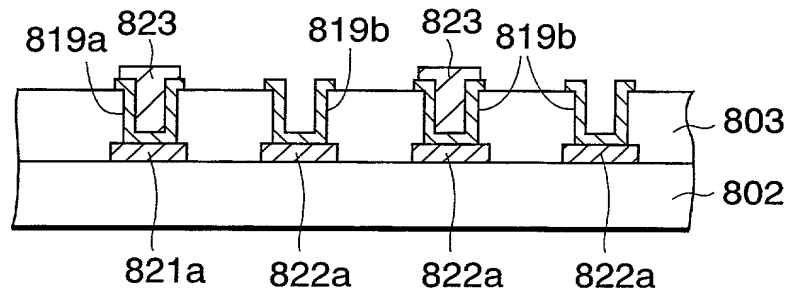


FIG.74B

